PMCL-217&219 SERVICE MANUAL

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PMCL-217 & 219 SERVICE MANUAL

1.PRECAUTION AND NOTICES

1.1. SAEFTY PRECAUTIONS

This monitor is manufactured and tested on a ground principle that user's safety comes First However improper use or installation may cause damage to the monitor well as to the user.
Carefully go over the following WARNINGS before installing and keep this guide handy.

WARNINGS

1. **This monitor should be operated only at the correct power sources indicated on the label on the rear end of the monitor. If you're unsure of the power supply in your residence, consult your local dealer or power company.**
2. **Use only the special power adapter that comes with this monitor for power input.**
3. **Do not try to repair the monitor yourself as it contains no user-serviceable parts, This monitor should only be repaired by a qualified technician.**
4. **Do not remove the monitor cabinet, There is high-voltage parts inside that may cause electric shock to human bodies, even when the power cord is unplugged .**
5. **Stop using the monitor if the cabinet is damaged, Have it checked by a service technician.**
6. **Put your monitor only in a clean, dry environment, I fit gets wet, unplug the power cable immediately and consult your service technician.**
7. **Always unplug the monitor before cleaning it, Clean the cabinet with a clean, dry cloth, Apply**
   non-ammonia based cleaner onto the cloth, not directly onto the glass screen.
8. **Keep the monitor away from magnetic objects motors, TV sets ,and transformer.**
9. **Do not place heavy objects on the monitor or power cord.**

1.2 PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in this chassis have special safety visual inspections and the protection afforded by them cannot necessarily be obtained by using replacement components rated for higher voltages wattage, etc. Before replacing any of these components read the parts list in this manual carefully. The use of substitute replacement parts which do not have the same safety characteristics as specified in the parts list may create shock, fire , or other hazards .

1.3SERVICE NOTES

1. When replacing parts or circuit boards, champ the lead wires around terminals before soldering.
2. When replacing a high wattage resistor (more than 1W of metal oxide film resistor) in circuit board, keep the resistor about 5mm away from circuit board.
3. Keep wires away from high voltage ,high temperature components and sharp edges .
4. Keep wires in their original position so as to reduce interference .
5. Usage of this product please refer to also user's manual.

2. SERVICE TOOL & EQUIPMENT REQUIRED

1. SIGNAL GEN
2 .MULTIMETER
3. OSCILLOSCOPE
4. SCREW DRIVER
5 .IRON
6. ABSORBER
7. SOLDER
8. DUMMY LOAD (5Ω/200W)

3. SPECIFICATIONS

3.1 PRODUCT SPECIFICATIONS

spec / Model	PMCL-217
LCD Display	17 ” active matrix TFT LCD panel
Pixel pitch	0.264 (H)×0.264 (V) mm
Display Resolution	1280 (H)×1024 (W)
Contrast Ratio	500:1
Brightness	300cd /㎡
Response Time	12ms
Display Color	16.2 M
Viewing Angle Left/Right	70°/70°
Up/Down	65°/65°
PC Input signal	Analog RGB 0.7Vp p
Display Mode	VGA /SVGA/XGA
PC input connector	15pin D-sub
Audio input signal	Analog stereo;1Vpp at 1KHz
Audio input connector	Line In Phone Jack
Speaker output	2×1.0 Watt built in
Front Control	Power on/off with LED ,Menu select, ← → ↓ ↑ Adjustment, Channel selection
OSD Menu	Contrast, Brightness, Display Adj, Audio Adj
Power Input	AC100~240V
Operating Consumption	50W max
Operating Conditions	Temperature:0℃~40℃ Humidity:20％~80％
Storage Conditions	Temperature:-20℃~60℃ Humidity:10％~90％
Safety Compliance	UL, CUL, CCC
EMI Compliance	FCC, CE
Dimensions	382(W)×78 (D)×342 (H)mm
Packing Dimensions	497(W)×195 (D)×463 (H)mm
Weight	N/W:5.5 Kgs G/W:6.7 Kgs

3. SPECIFICATIONS

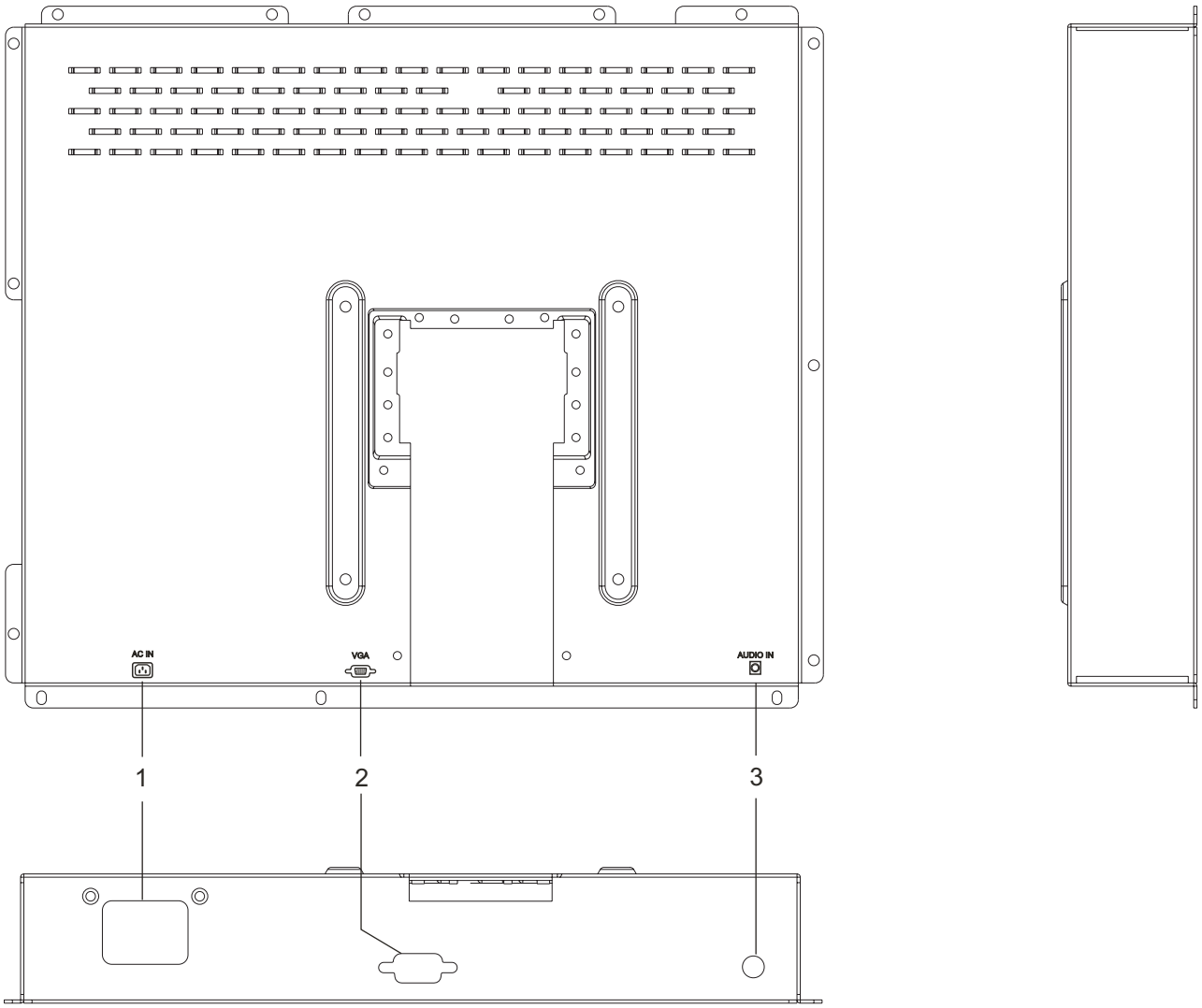
3.1 PRODUCT SPECIFICATIONS

Model / spec	PMCL-219
LCD Display	19 ” active matrix TFT LCD panel
Pixel pitch	0.294 (H)×0.294 (V) mm
Display Resolution	1280 (H)×1024 (W)
Contrast Ratio	500:1
Brightness	250cd /㎡
Response Time	8ms
Display Color	16.2 M
Viewing Angle Left/Right	75°/75°
Up/Down	75°/60°
PC Input signal	Analog RGB 0.7Vp p
Display Mode	VGA /SVGA/XGA
PC input connector	15pin D-sub
Audio input signal	Analog stereo;1Vpp at 1KHz
Audio input connector	Line In Phone Jack
Speaker output	2×1.0 Watt built in
Front Control	Power on/off with LED ,Menu select, ← → ↓ ↑ Adjustment, Channel selection
OSD Menu	Contrast, Brightness, Display Adj, Audio Adj
Power Input	AC100~240V
Operating Consumption	50W max
Operating Conditions	Temperature:0℃~40℃ Humidity:20％~80％
Storage Conditions	Temperature:-20℃~60℃ Humidity:10％~90％
Safety Compliance	UL, CUL, CCC
EMI Compliance	FCC, CE
Dimensions	420(W)×81 (D)×374 (H)mm
Packing Dimensions	535 (W)×195 (D)×495 (H)mm
Weight	N/W:6.5 Kgs G/W:8.2 Kgs

3.2 SUPPORTED INPUT MODE

Mode	Resolution	H Freq(kHz)	V Freq(kHz)
0	720*350	31.469	70.087
1	640*480	31.469	59.940
2	640*480	35.000	66.667
3	640*480	37.861	72.809
4	640*480	37.500	75.000
5	720*400	31.469	70.087
6	800*600	35.156	56.250
7	800*600	37.879	60.317
8	800*600	48.077	72.188
9	800*600	46.875	75.000
10	832*624	49.750	74.500
11	1024*768	48.363	60.004
12	1024*768	56.476	70.069
13	1024*768	58.038	71.918
14	1024*768	60.023	75.029
15	1280*1024	64.000	60.000
16	1280*1024	80.000	75.000

3. 3 REAR INPUTS

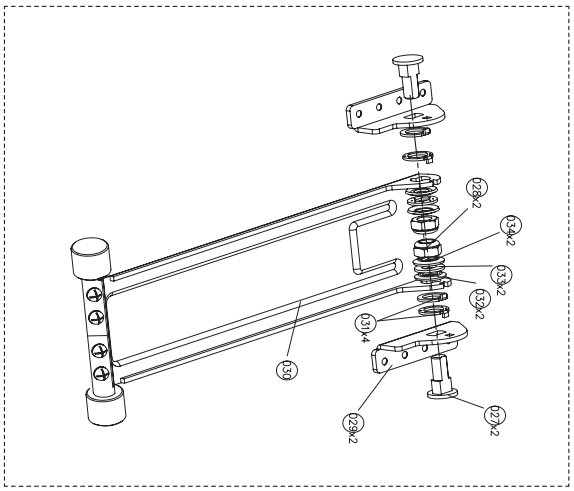


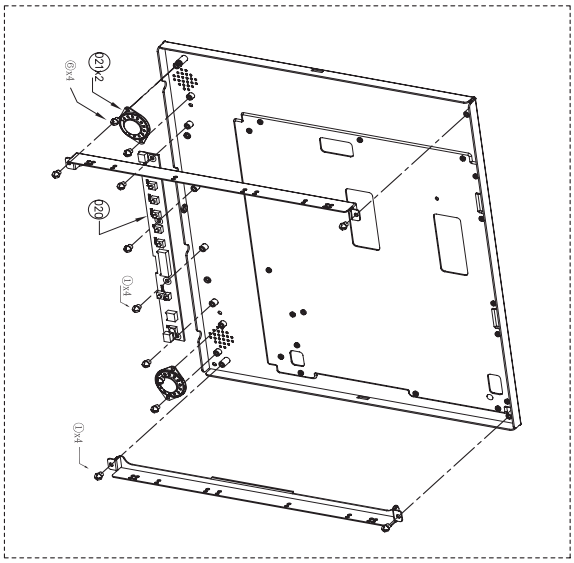
1. AC IN This can be connected to AC 100 ~ 240V 50/60 Hz

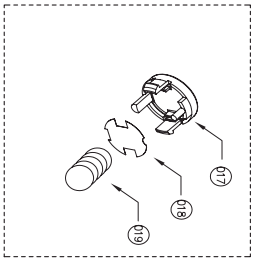
2.VGA IN: This is to be connected to the D-Sub 15 pin signal connector

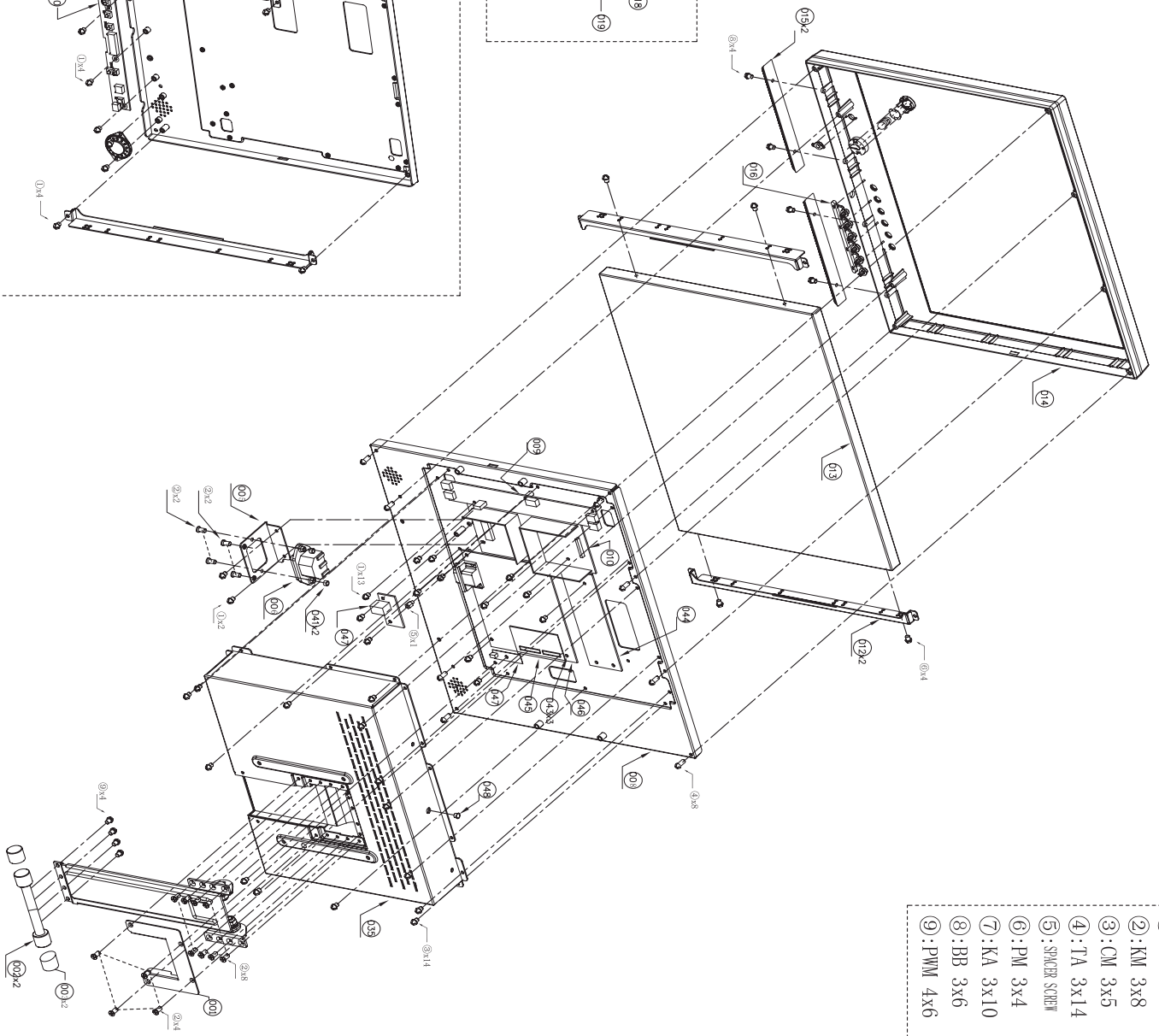
3.LINE IN: This can be connected to the stereo audio out of any sound resouse

4. EXPLODED VIEW





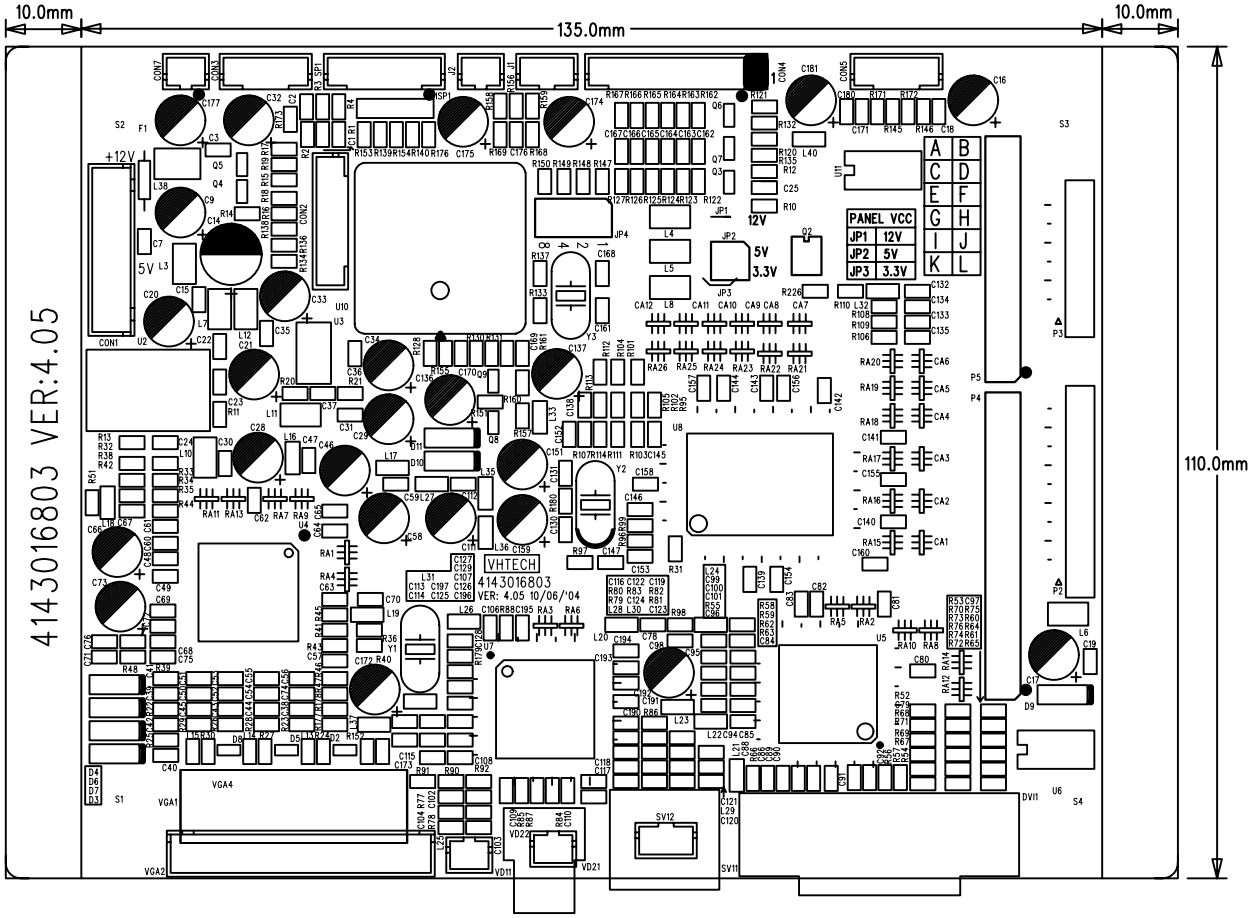


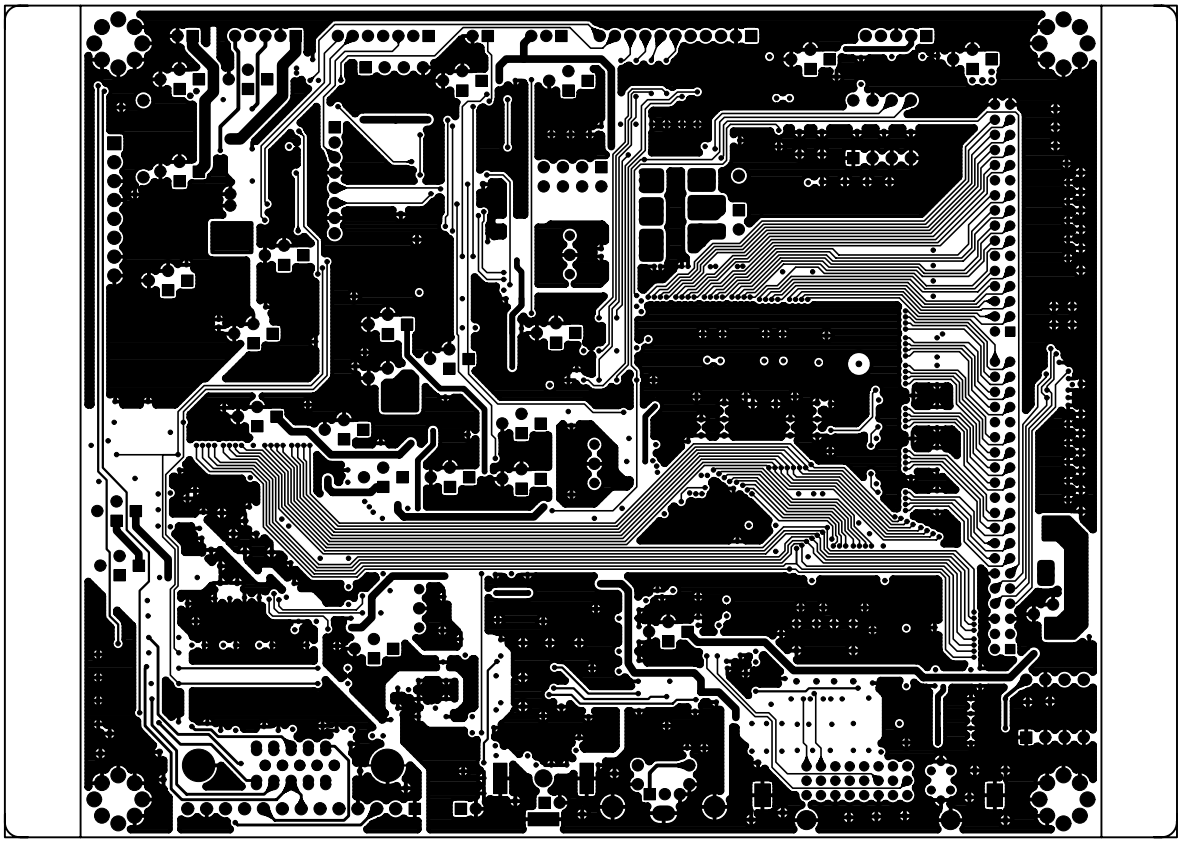


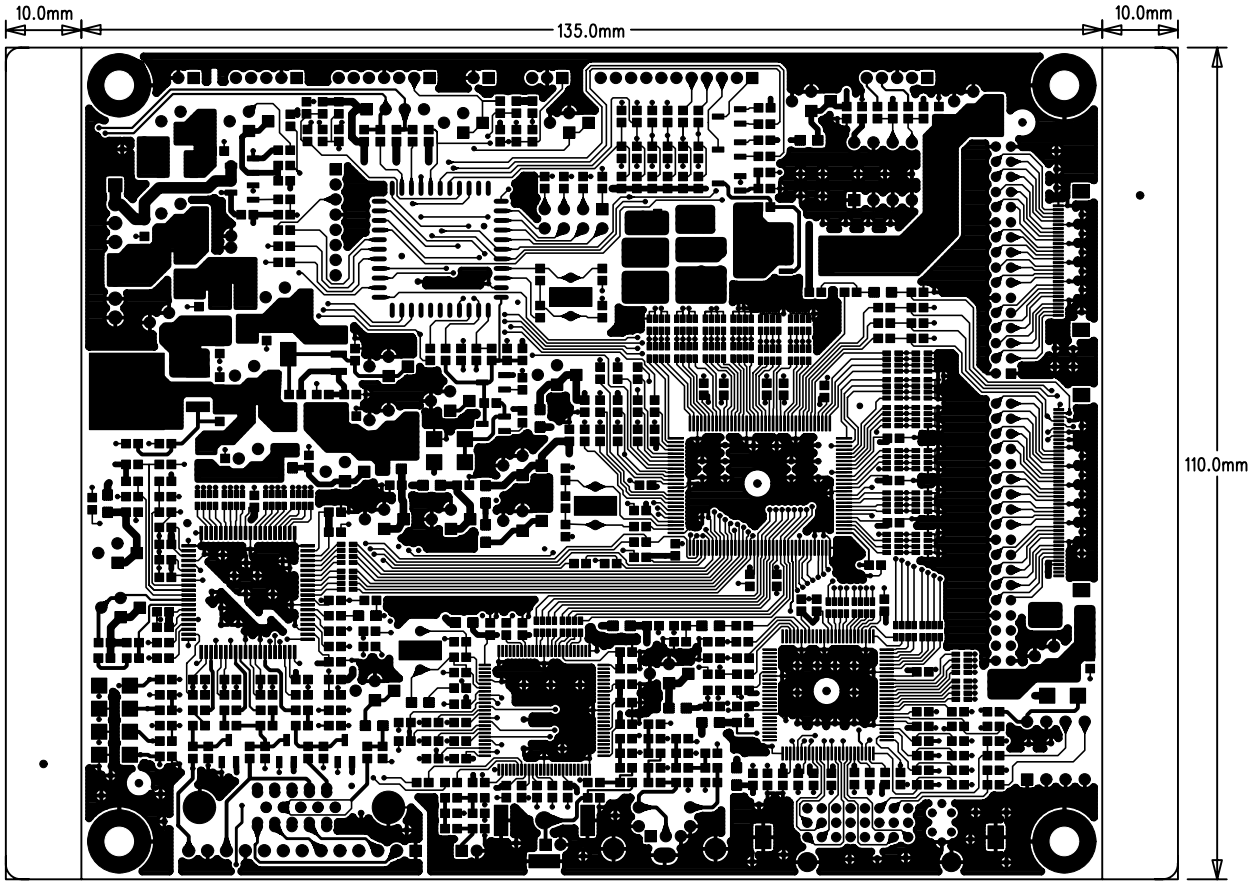
①:PWM 3x6
②:KM 3x8
③:CM 3x5
④:TA 3x14
⑤:SPACER SCREW
⑥:PM 3x4
⑦:KA 3x10
⑧:BB 3x6
⑨:PWM 4x6

5. PCB LAYOUT

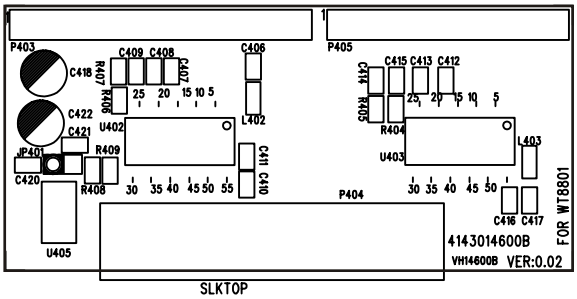
5. 1 MAIN PCB LAYOUT

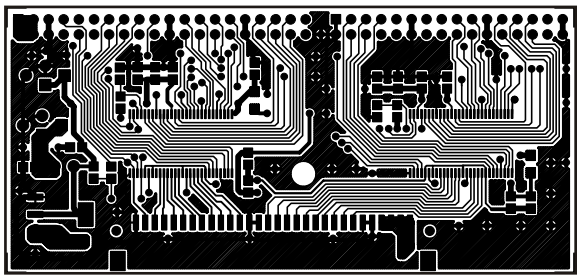




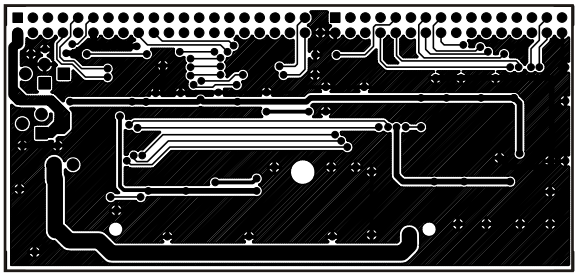


5. 2 CONVERTER PCB LAYOUT



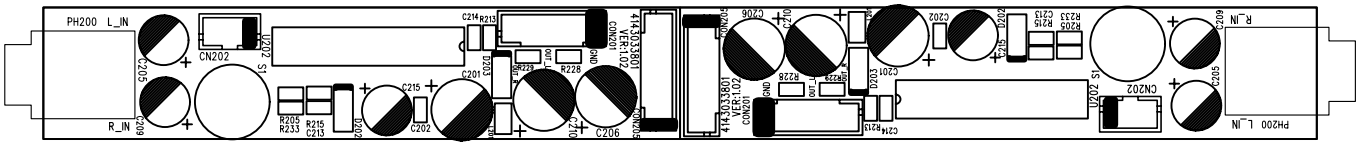


COMP

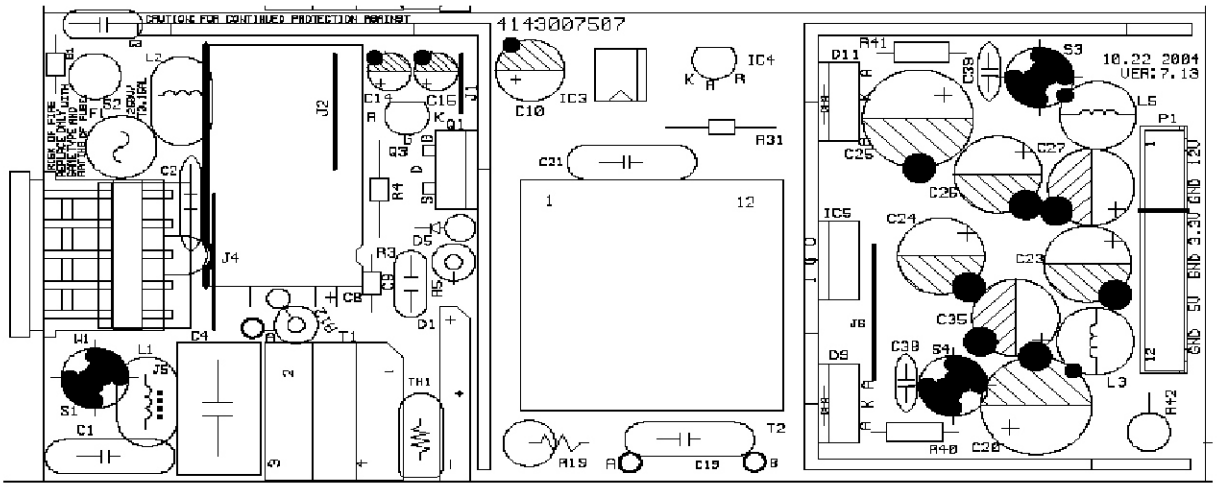


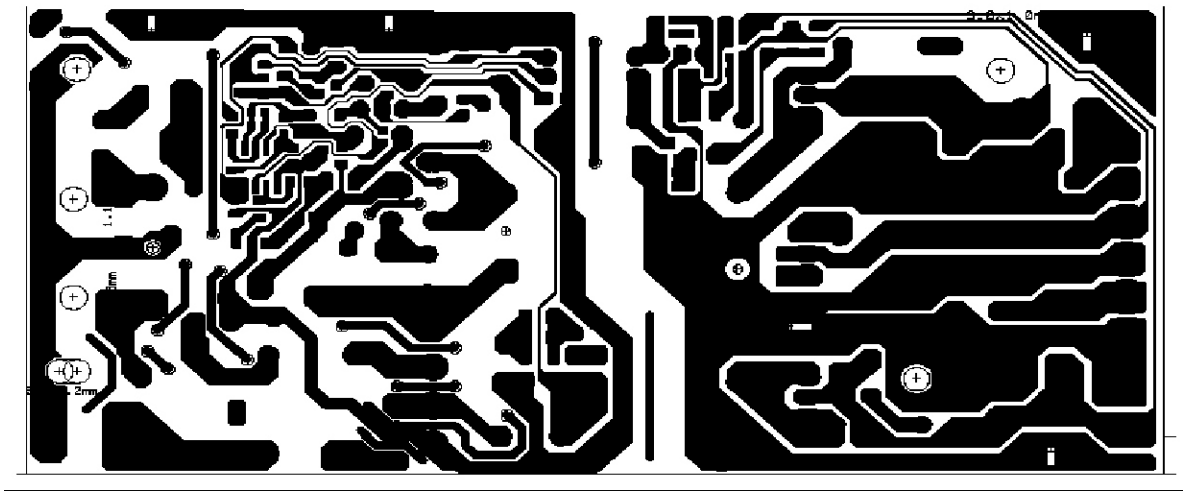
SOLDER

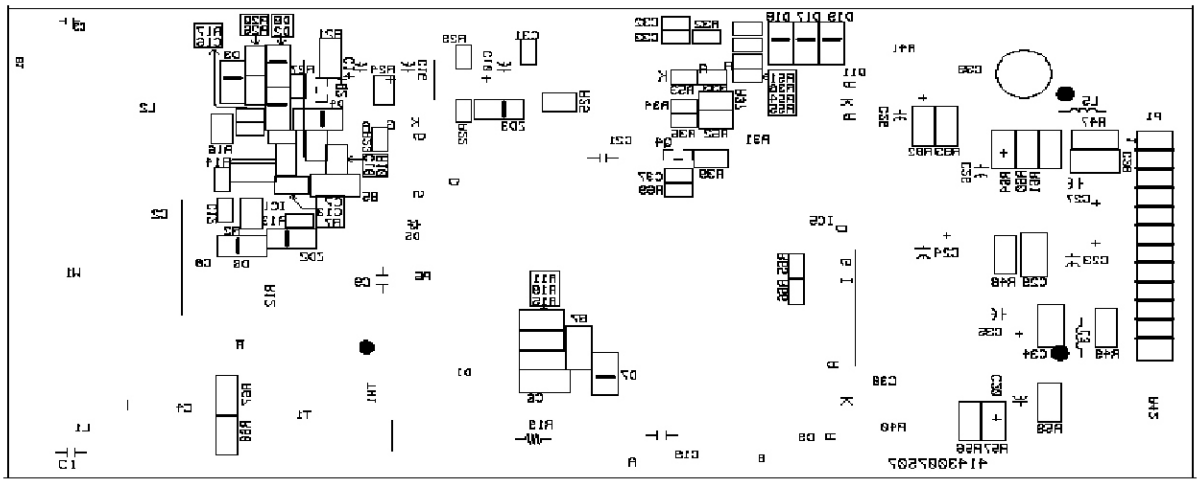
5. 3 AUDIO PCB LAYOUT



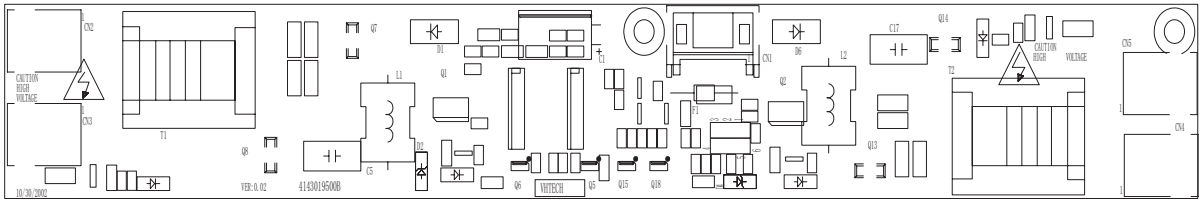
5. 4 POWER PCB LAYOUT

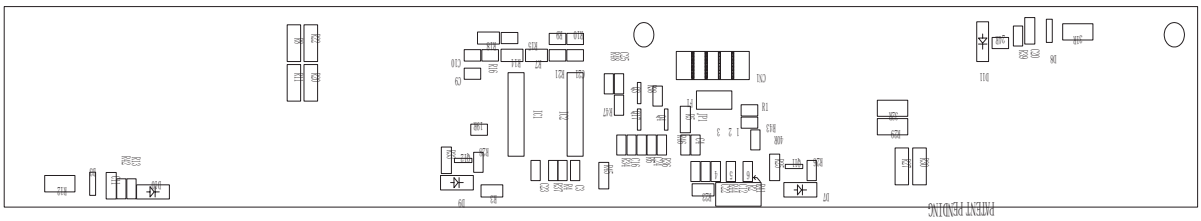


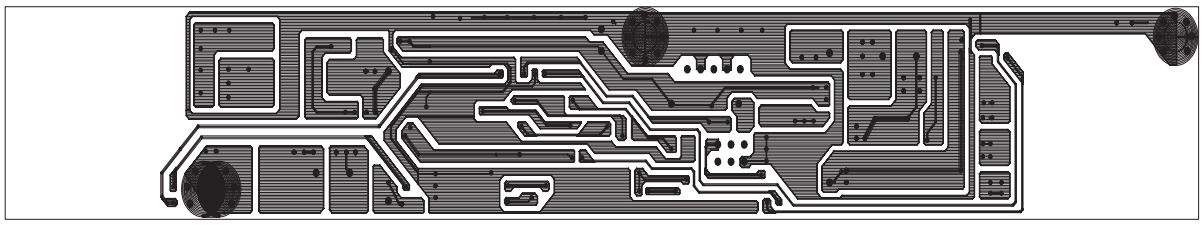


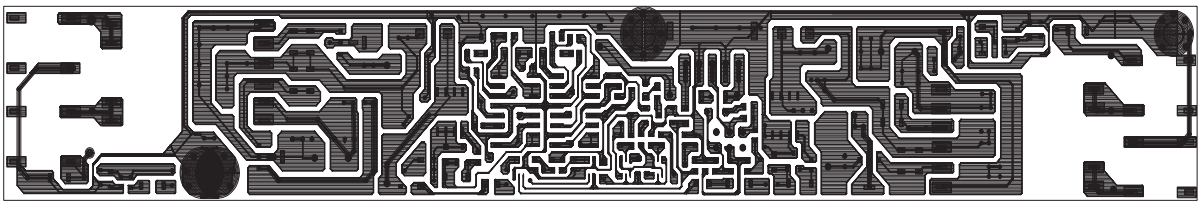


5. 5 INVERTER PCB LAYOUT

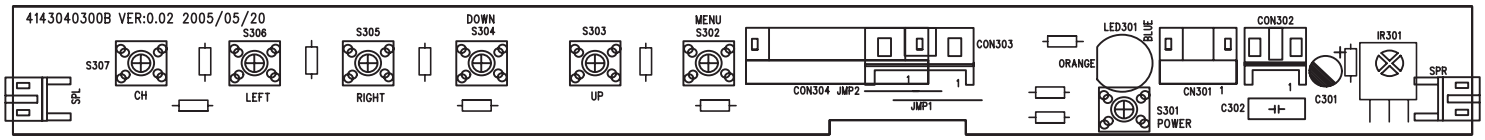




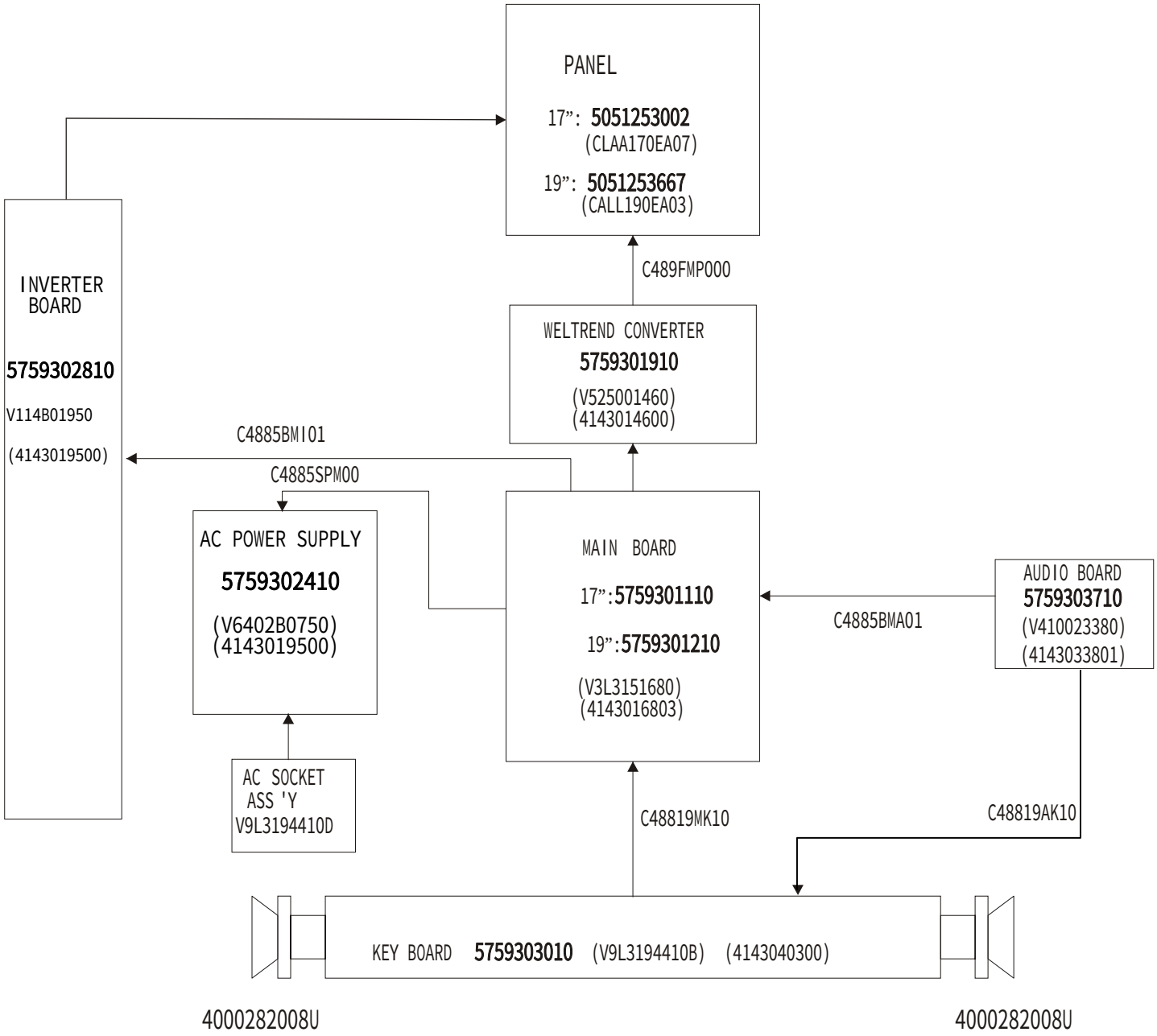




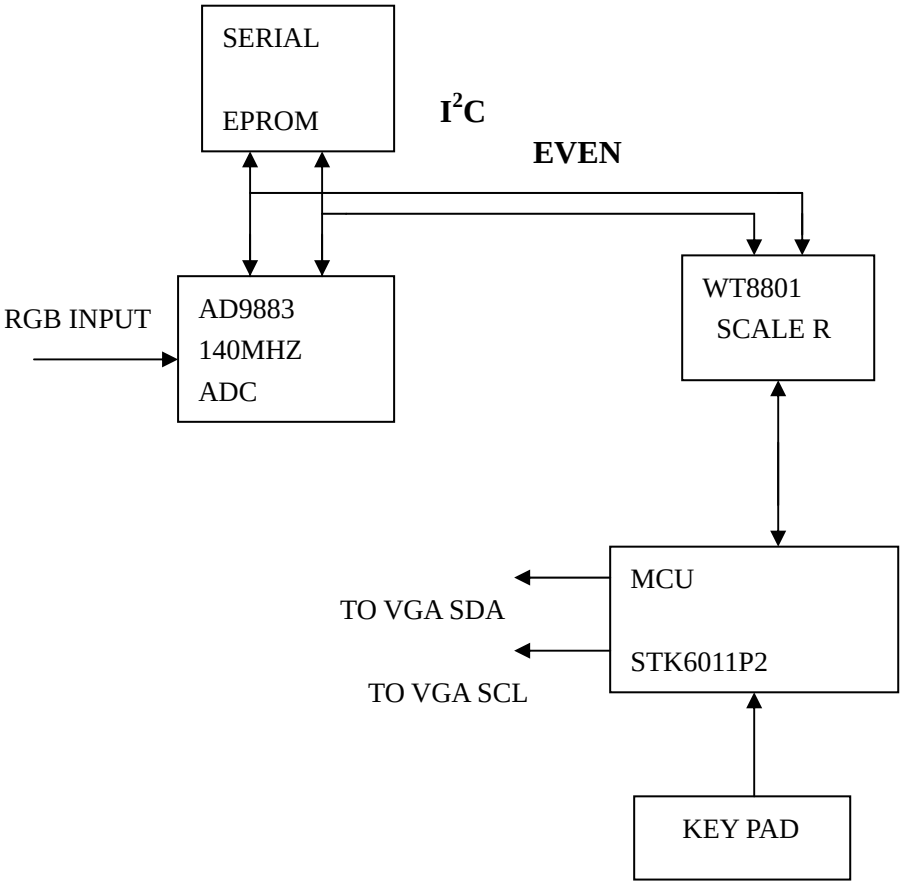
5. 6 KEY PCB LAYOUT



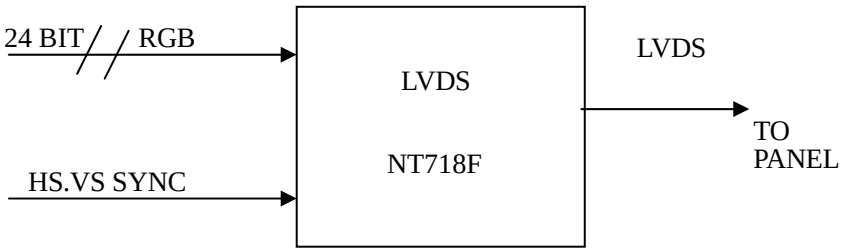
6.PMCL-217 & 219 BLOCK DIAGRAM



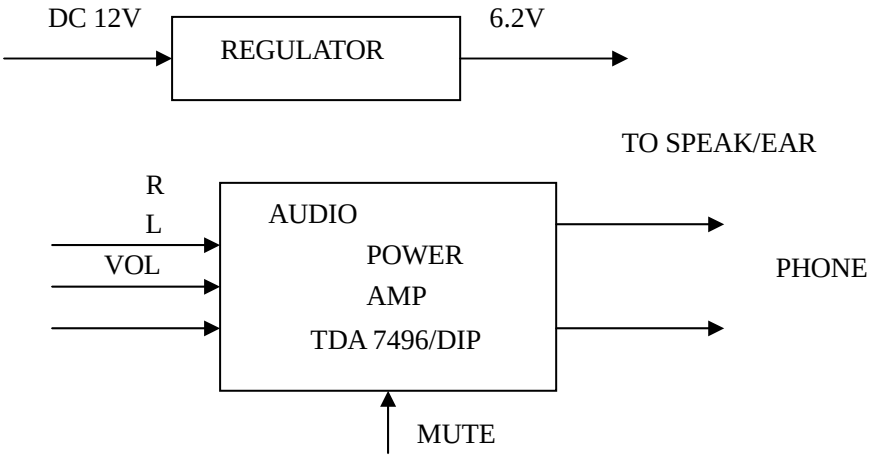
6.1 MAIN BLOCK DIAGRAM



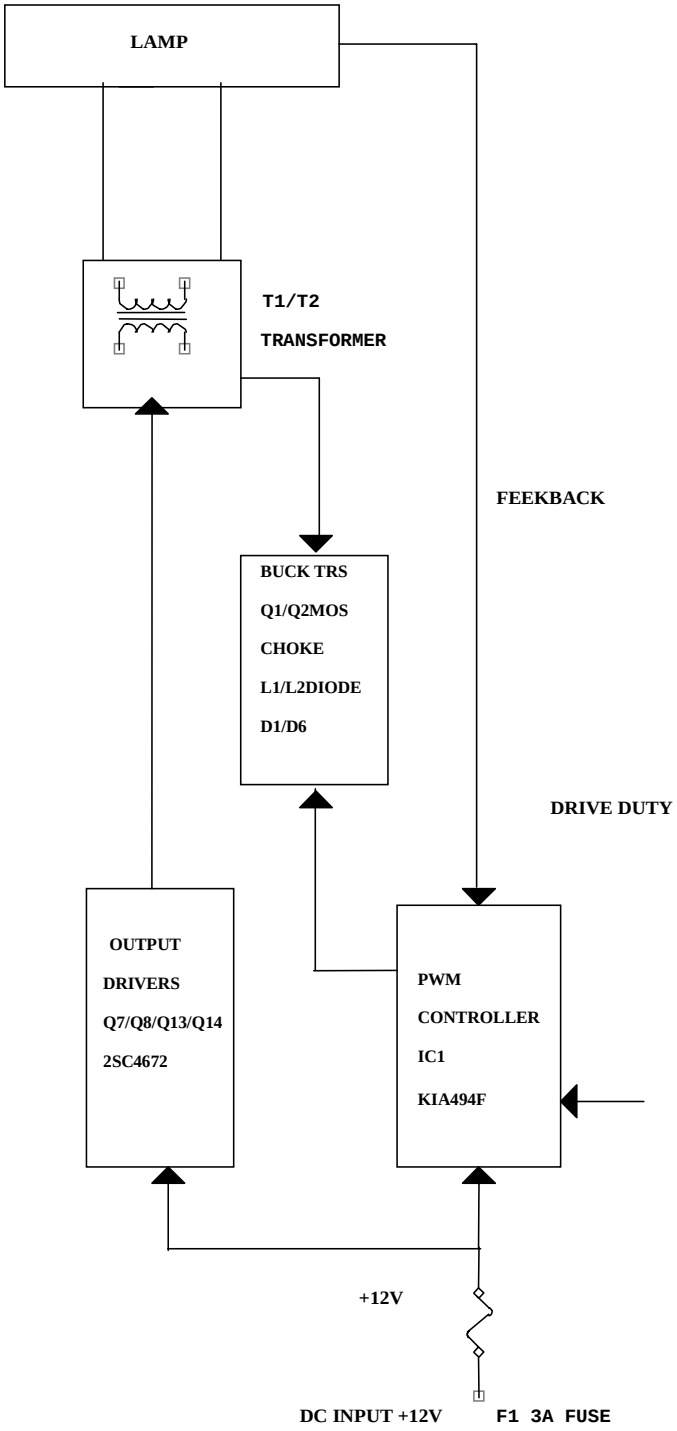
6.2 CONVERTER BLOCK DIAGRAM



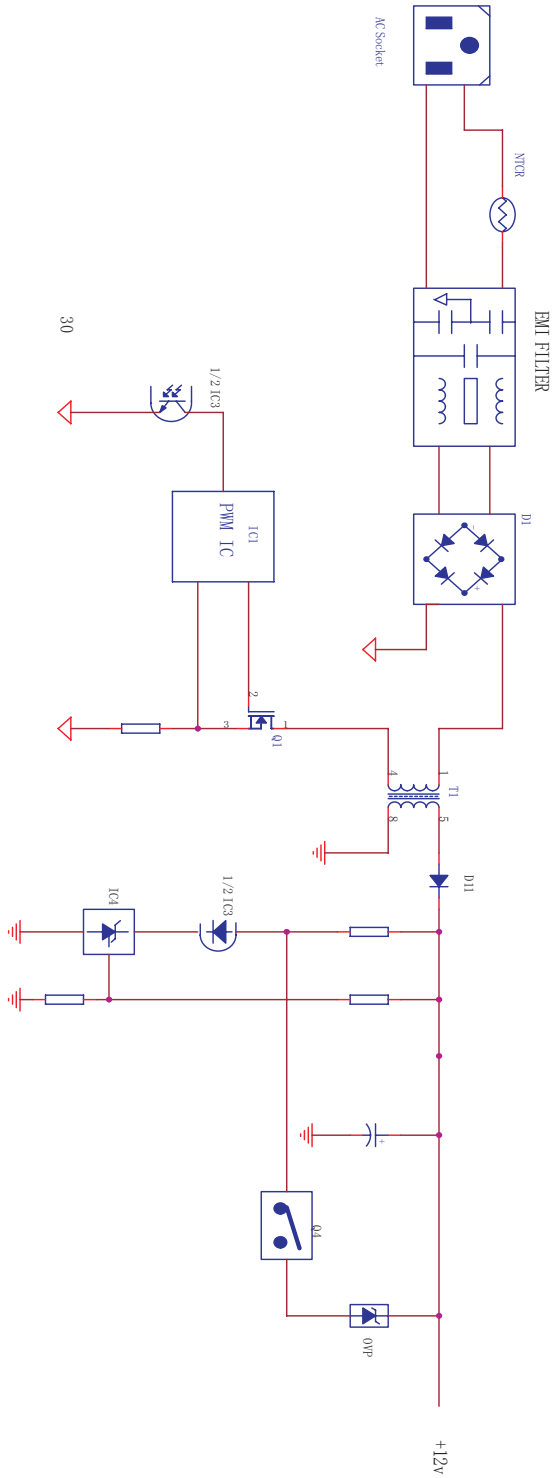
6.3 AUDIO BLOCK DIAGRAM



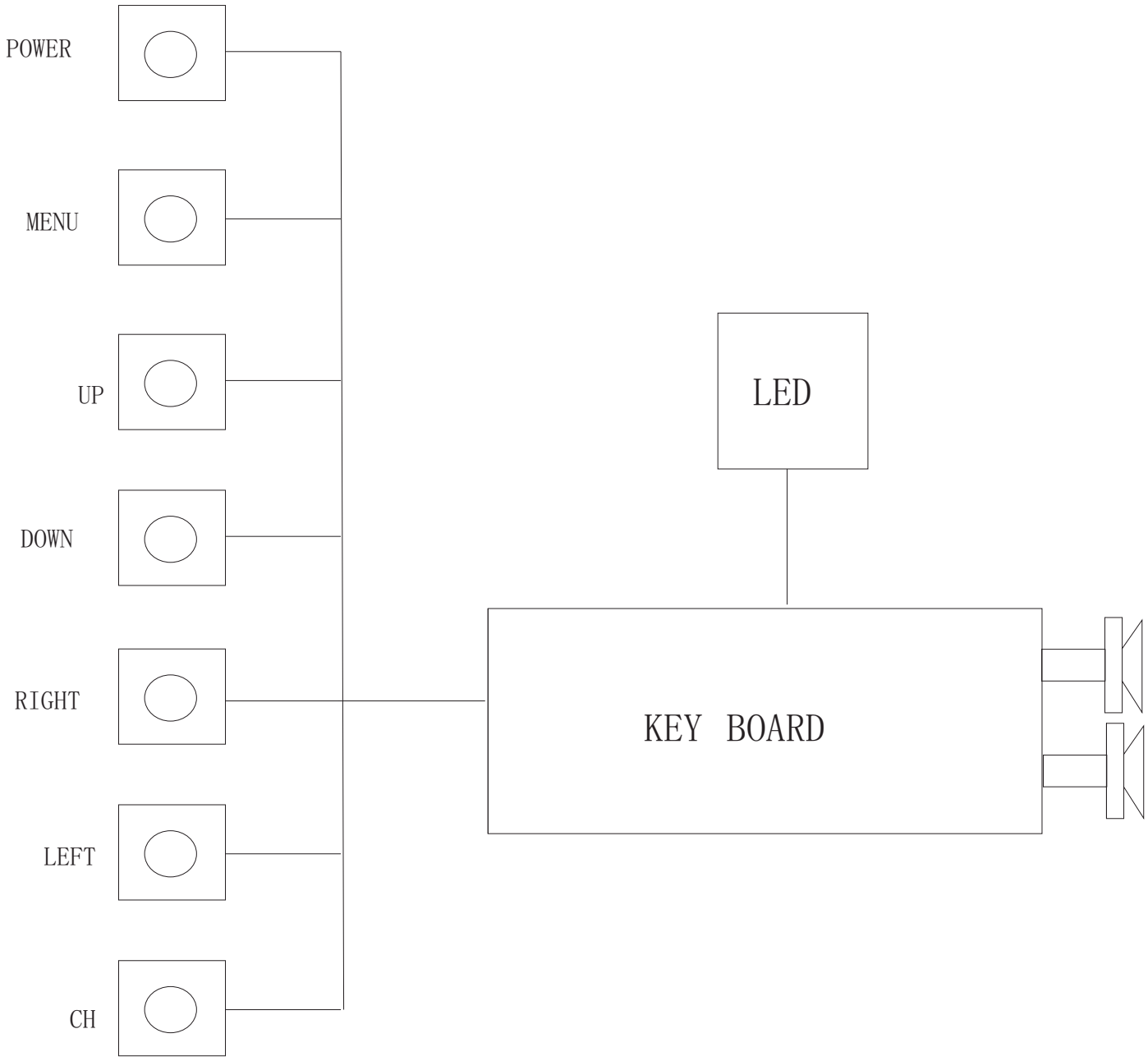
6.4 INVERTER BLOCK DIAGRAM



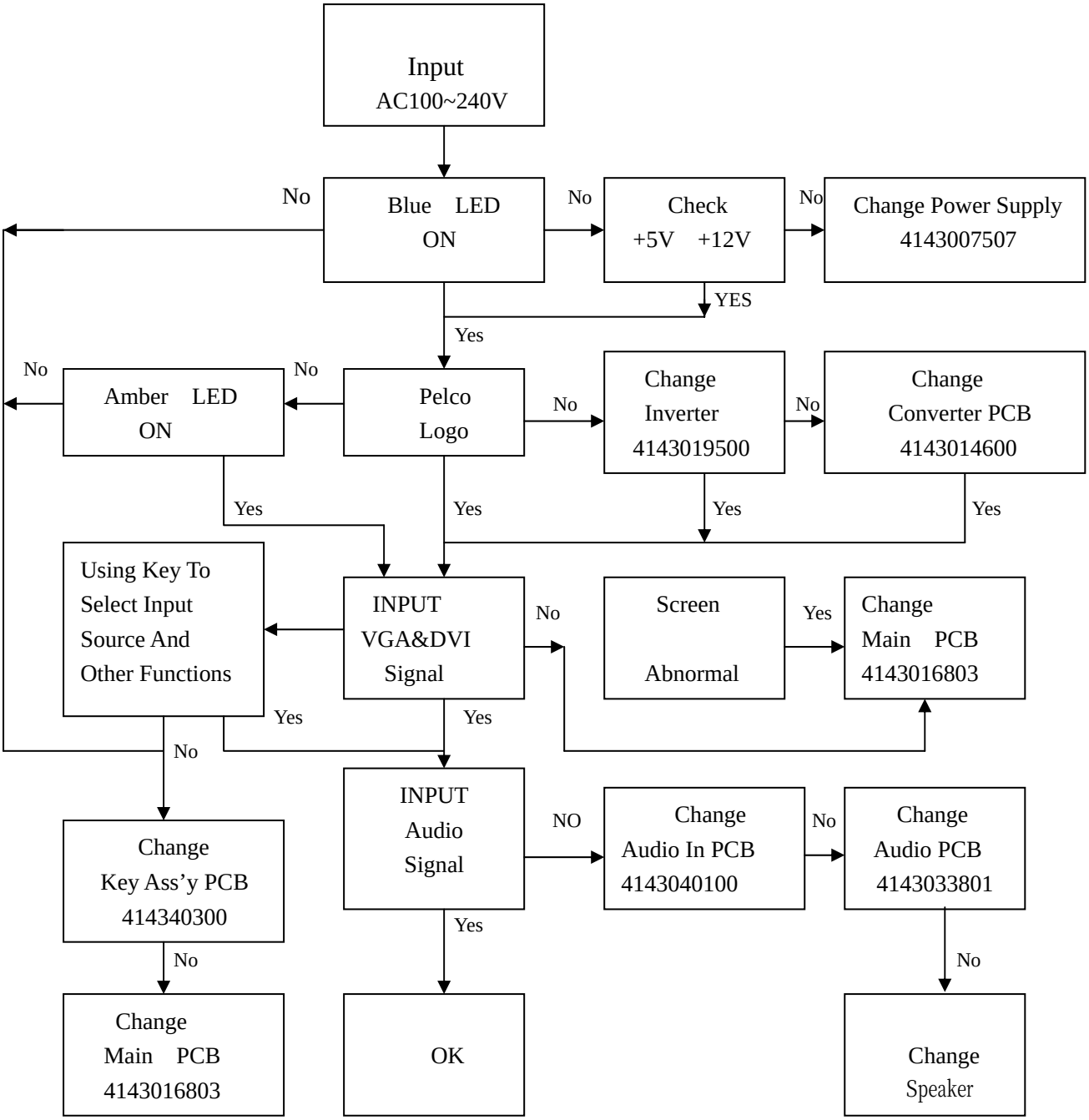
6. 5 POWER BLOCK DIAGRAM



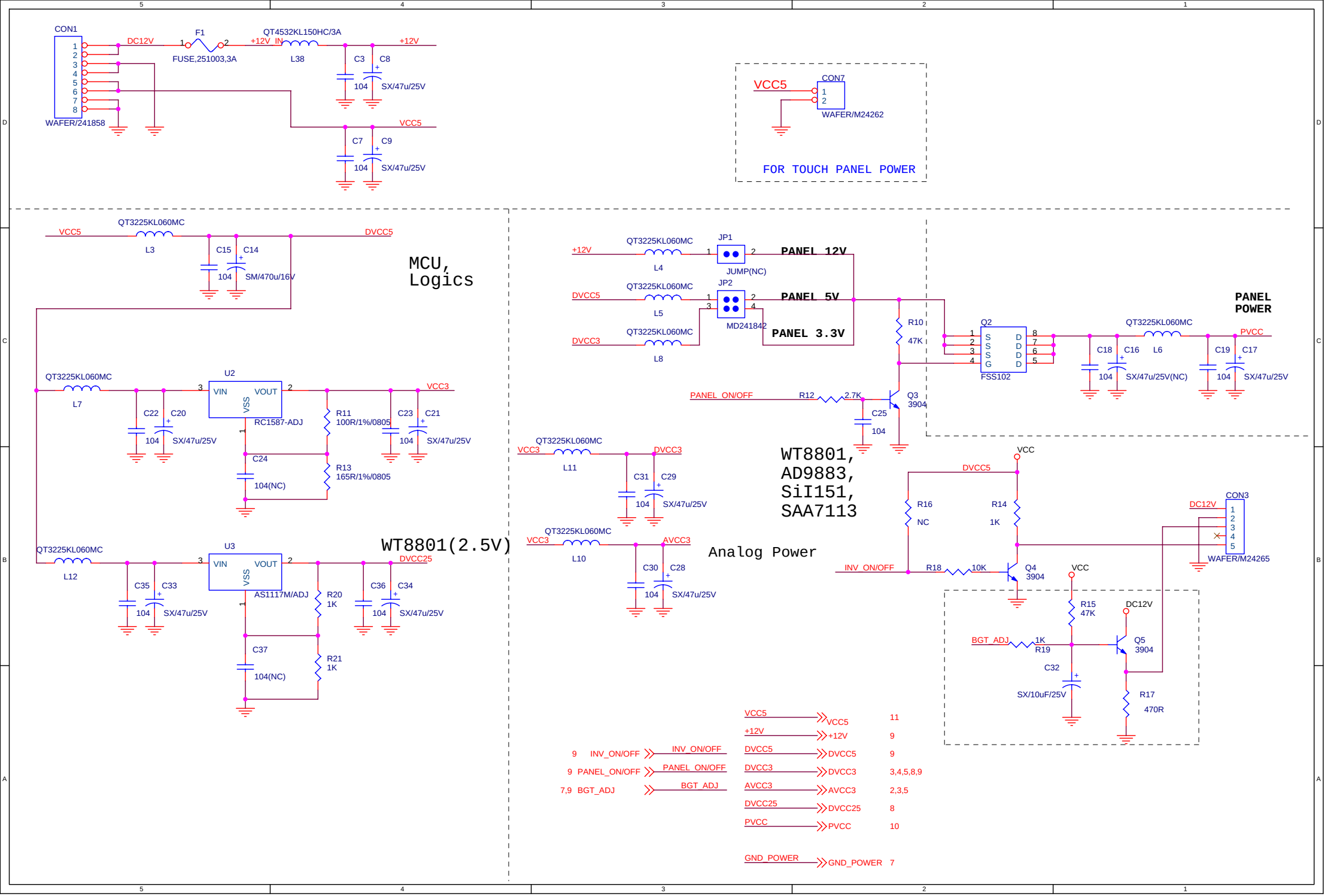
6.6 KEY BLOCK DIAGRAM

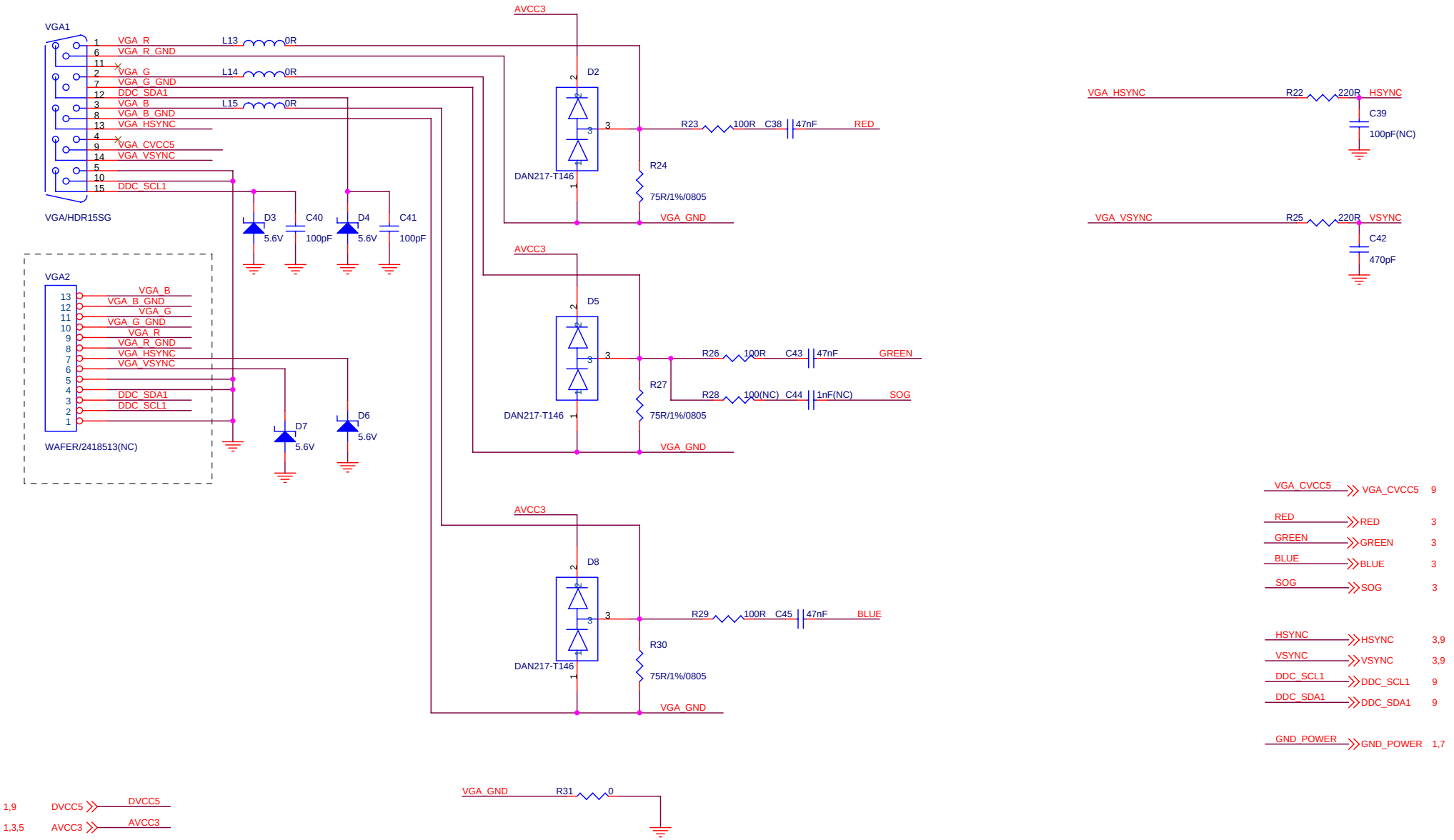


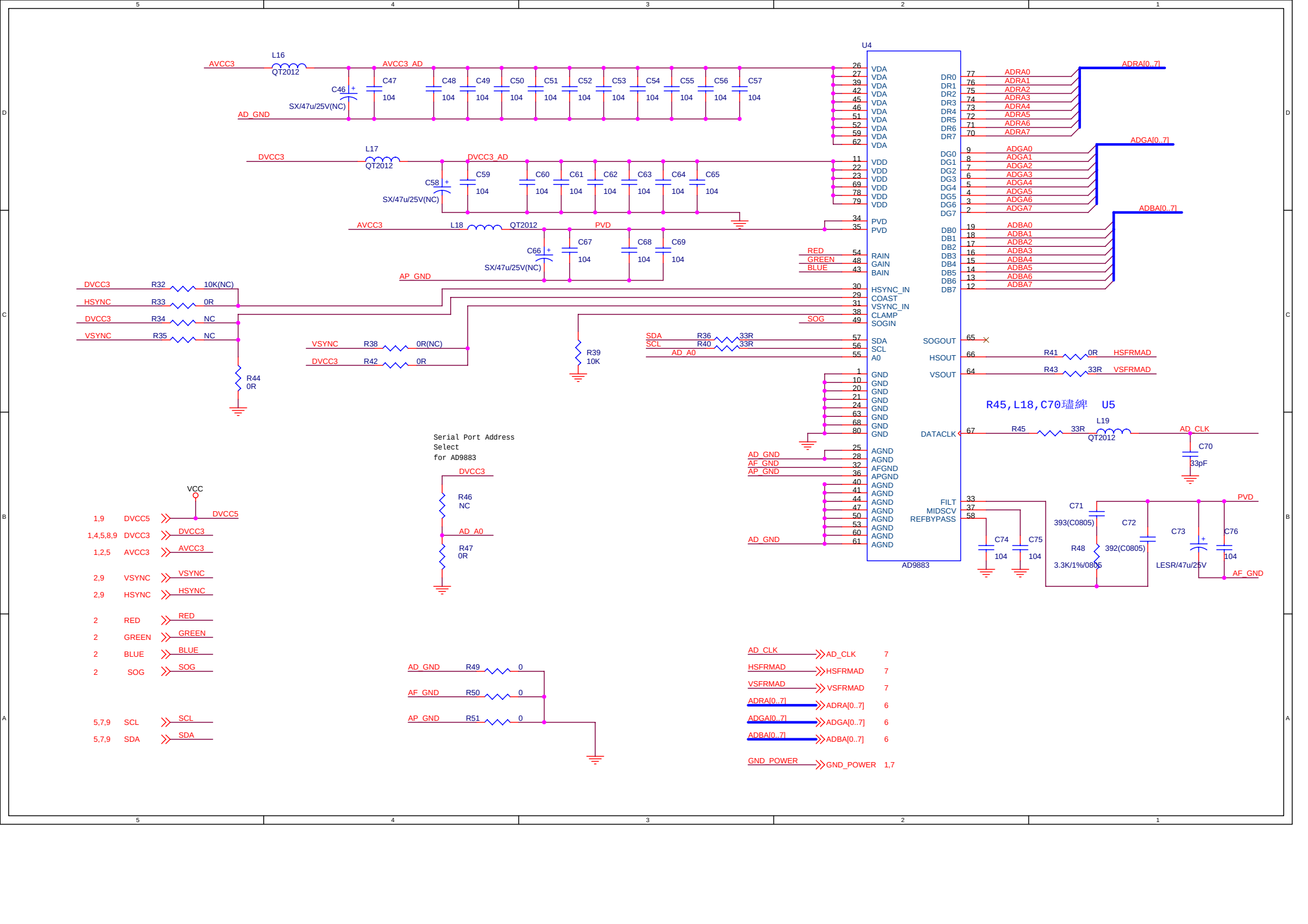
7. TROUBLE SHOOTING FLOW CHART

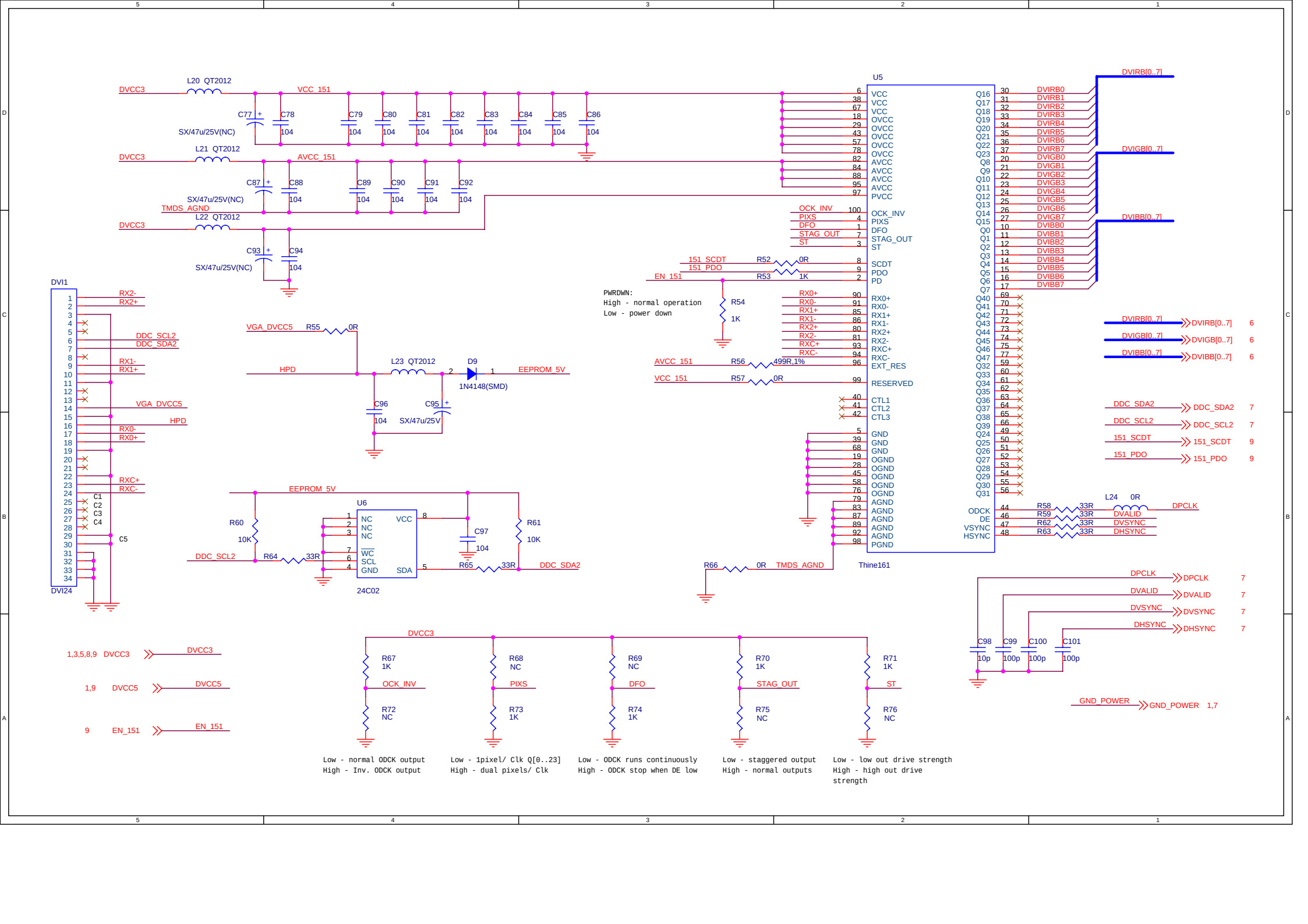


8.SCHEMATIC

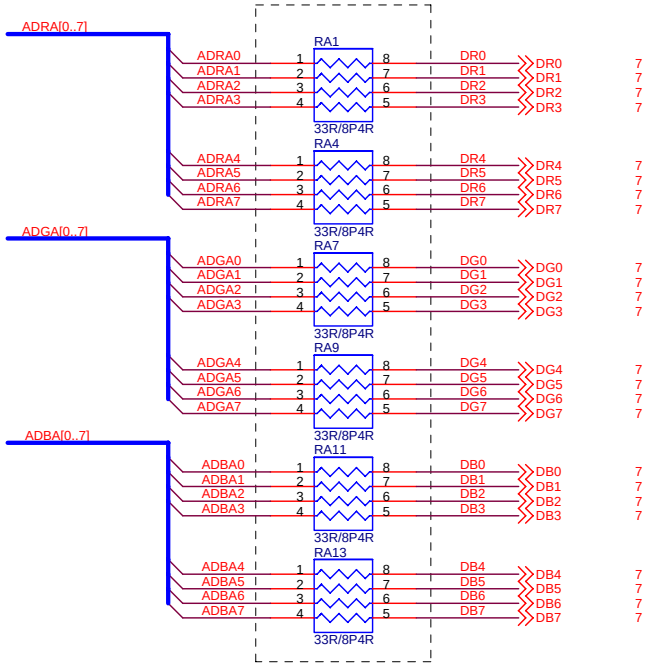

CON1
DC12V
F1
FUSE,251003,3A
QT4532KL150HC/3A
+12V_IN
L38
+12V
C3
C8
104
SX/47u/25V
WAFER/241858
VCC5
C7
C9
104
SX/47u/25V
VCC5
CON7
WAFER/M24262
FOR TOUCH PANEL POWER
MCU,
Logics
QT3225KL060MC
VCC5
L3
C15
C14
104
SM/470u/16V
DVCC5
QT3225KL060MC
L7
U2
VIN
VOUT
VSS
RC1587-ADJ
C22
C20
104
SX/47u/25V
C24
104(NC)
R11
100R/1%/0805
R13
165R/1%/0805
C23
C21
104
SX/47u/25V
VCC3
WT8801(2.5V)
QT3225KL060MC
L12
U3
VIN
VOUT
VSS
AS1117M/ADJ
C35
C33
104
SX/47u/25V
C37
104(NC)
R20
1K
R21
1K
C36
C34
104
SX/47u/25V
DVCC25
+12V
QT3225KL060MC
L4
JP1
JUMP(NC)
PANEL 12V
DVCC5
QT3225KL060MC
L5
JP2
MD241842
PANEL 5V
DVCC3
QT3225KL060MC
L8
PANEL 3.3V
R10
47K
Q2
FSS102
PANEL
POWER
QT3225KL060MC
L6
C18
C16
104
SX/47u/25V(NC)
PVCC
C19
C17
104
SX/47u/25V
PANEL_ON/OFF
R12
2.7K
Q3
3904
C25
104
WT8801,
AD9883,
SiI151,
SAA7113
Analog Power
QT3225KL060MC
VCC3
L11
C31
C29
104
SX/47u/25V
DVCC3
QT3225KL060MC
VCC3
L10
C30
C28
104
SX/47u/25V
AVCC3
VCC
DVCC5
R16
NC
R14
1K
INV_ON/OFF
R18
10K
Q4
3904
VCC
CON3
DC12V
WAFER/M24265
R15
47K
DC12V
BGT_ADJ
1K
R19
Q5
3904
C32
SX/10uF/25V
R17
470R
VCC5
VCC5
11
+12V
+12V
9
9
INV_ON/OFF
INV_ON/OFF
DVCC5
DVCC5
9
9
PANEL_ON/OFF
PANEL_ON/OFF
DVCC3
DVCC3
3,4,5,8,9
7,9
BGT_ADJ
BGT_ADJ
AVCC3
AVCC3
2,3,5
DVCC25
DVCC25
8
PVCC
PVCC
10
GND_POWER
GND_POWER
7


VGA1
VGA_R
VGA_R_GND
VGA_G
VGA_G_GND
DDC_SDA1
VGA_B
VGA_B_GND
VGA_HSYNC
VGA_CVCC5
VGA_VSYNC
DDC_SCL1
L13 0R
L14 0R
L15 0R
VGA/HDR15SG
D3 5.6V
C40 100pF
D4 5.6V
C41 100pF
VGA2
WAFER/2418513(NC)
D7 5.6V
D6 5.6V
AVCC3
D2
DAN217-T146
R23 100R C38 47nF RED
R24 75R/1%/0805
VGA_GND
D5
R26 100R C43 47nF GREEN
R27
R28 100(NC) C44 1nF(NC) SOG
D8
R29 100R C45 47nF BLUE
R30
VGA_HSYNC
R22 220R HSYNC
C39 100pF(NC)
VGA_VSYNC
R25 220R VSYNC
C42 470pF
VGA_CVCC5 VGA_CVCC5 9
RED RED 3
GREEN GREEN 3
BLUE BLUE 3
SOG SOG 3
HSYNC HSYNC 3,9
VSYNC VSYNC 3,9
DDC_SCL1 DDC_SCL1 9
DDC_SDA1 DDC_SDA1 9
GND_POWER GND_POWER 1,7
1,9 DVCC5 DVCC5
1,3,5 AVCC3 AVCC3
VGA_GND R31 0

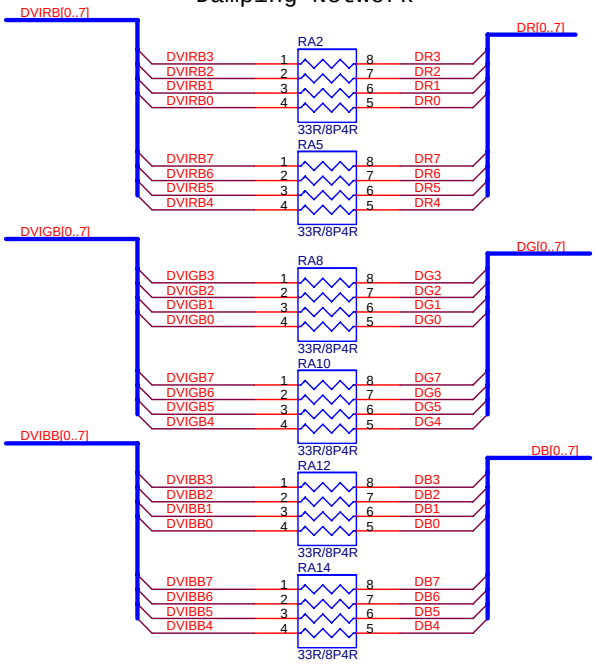

U4
AD9883
L16
QT2012
AVCC3
AVCC3_AD
AD_GND
C46
SX/47u/25V(NC)
C47
C48
C49
C50
C51
C52
C53
C54
C55
C56
C57
104
L17
DVCC3
DVCC3_AD
C58
C59
C60
C61
C62
C63
C64
C65
L18
PVD
C66
C67
C68
C69
AP_GND
VDA
VDD
PVD
RAIN
GAIN
BAIN
RED
GREEN
BLUE
HSYNC_IN
COAST
VSYNC_IN
CLAMP
SOGIN
SOG
SDA
SCL
A0
GND
AGND
AFGND
APGND
DR0
DR1
DR2
DR3
DR4
DR5
DR6
DR7
DG0
DG1
DG2
DG3
DG4
DG5
DG6
DG7
DB0
DB1
DB2
DB3
DB4
DB5
DB6
DB7
ADRA0
ADRA1
ADRA2
ADRA3
ADRA4
ADRA5
ADRA6
ADRA7
ADRA[0..7]
ADGA0
ADGA1
ADGA2
ADGA3
ADGA4
ADGA5
ADGA6
ADGA7
ADGA[0..7]
ADBA0
ADBA1
ADBA2
ADBA3
ADBA4
ADBA5
ADBA6
ADBA7
ADBA[0..7]
SOGOUT
HSOUT
VSOUT
DATACLK
FILT
MIDSCV
REFBYPASS
R32 10K(NC)
R33 0R
R34 NC
R35 NC
R44 0R
R38 0R(NC)
R42 0R
R39 10K
R36 33R
R40 33R
R41 0R HSFRMAD
R43 33R VSFRMAD
R45,L18,C70璩緛 U5
R45 33R
L19
C70 33pF
C71 393(C0805)
C72
R48 392(C0805) 3.3K/1%/0805
C73
C74 104
C75 104
C76 104
LESR/47u/25V
PVD
AF_GND
Serial Port Address Select for AD9883
R46 NC
R47 0R
AD_A0
VCC
DVCC5
DVCC3
AVCC3
VSYNC
HSYNC
RED
GREEN
BLUE
SOG
SCL
SDA
R49 0
R50 0
R51 0
AD_CLK
HSFRMAD
VSFRMAD
GND_POWER
GND_POWER 1,7


DVCC3
L20 QT2012
VCC_151
C77 SX/47u/25V(NC)
C78 104
C79 104
C80 104
C81 104
C82 104
C83 104
C84 104
C85 104
C86 104
L21 QT2012
AVCC_151
C87 SX/47u/25V(NC)
C88 104
C89 104
C90 104
C91 104
C92 104
TMDS_AGND
L22 QT2012
C93 SX/47u/25V(NC)
C94 104
U5
Thine161
VCC
OVCC
AVCC
PVCC
OCK_INV
PIXS
DFO
STAG_OUT
ST
SCDT
PDO
PD
RX0+
RX0-
RX1+
RX1-
RX2+
RX2-
RXC+
RXC-
EXT_RES
RESERVED
CTL1
CTL2
CTL3
GND
OGND
AGND
PGND
ODCK
DE
VSYNC
HSYNC
151_SCDT
151_PDO
EN_151
R52 0R
R53 1K
PWRDWN:
High - normal operation
Low - power down
R54 1K
AVCC_151
R56 499R,1%
VCC_151
R57 0R
R66 0R
TMDS_AGND
DVIRB[0..7]
DVIGB[0..7]
DVIBB[0..7]
DVIRB0
DVIRB1
DVIRB2
DVIRB3
DVIRB4
DVIRB5
DVIRB6
DVIRB7
DVIGB0
DVIGB1
DVIGB2
DVIGB3
DVIGB4
DVIGB5
DVIGB6
DVIGB7
DVIBB0
DVIBB1
DVIBB2
DVIBB3
DVIBB4
DVIBB5
DVIBB6
DVIBB7
DVIRB[0..7] DVIRB[0..7] 6
DVIGB[0..7] DVIGB[0..7] 6
DVIBB[0..7] DVIBB[0..7] 6
DDC_SDA2 DDC_SDA2 7
DDC_SCL2 DDC_SCL2 7
151_SCDT 151_SCDT 9
151_PDO 151_PDO 9
L24 0R
R58 33R DPCLK
R59 33R DVALID
R62 33R DVSYNC
R63 33R DHSYNC
DPCLK DPCLK 7
DVALID DVALID 7
DVSYNC DVSYNC 7
DHSYNC DHSYNC 7
C98 10p
C99 100p
C100 100p
C101 100p
GND_POWER GND_POWER 1,7
DVI1
DVI24
RX2-
RX2+
DDC_SCL2
DDC_SDA2
RX1-
RX1+
VGA_DVCC5
HPD
RX0-
RX0+
RXC+
RXC-
C1
C2
C3
C4
C5
VGA_DVCC5
R55 0R
HPD
L23 QT2012
D9
1N4148(SMD)
EEPROM_5V
C96 104
C95 SX/47u/25V
EEPROM_5V
U6
24C02
NC
WC
SCL
SDA
VCC
GND
R60 10K
R61 10K
C97 104
DDC_SCL2
R64 33R
R65 33R
DDC_SDA2
1,3,5,8,9 DVCC3 DVCC3
1,9 DVCC5 DVCC5
9 EN_151 EN_151
DVCC3
R67 1K
R68 NC
R69 NC
R70 1K
R71 1K
OCK_INV
PIXS
DFO
STAG_OUT
ST
R72 NC
R73 1K
R74 1K
R75 NC
R76 NC
Low - normal ODCK output
High - Inv. ODCK output
Low - 1pixel/ Clk Q[0..23]
High - dual pixels/ Clk
Low - ODCK runs continuously
High - ODCK stop when DE low
Low - staggered output
High - normal outputs
Low - low out drive strength
High - high out drive strength

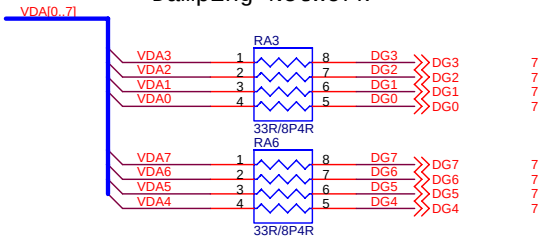
Analog Interface Damping Network

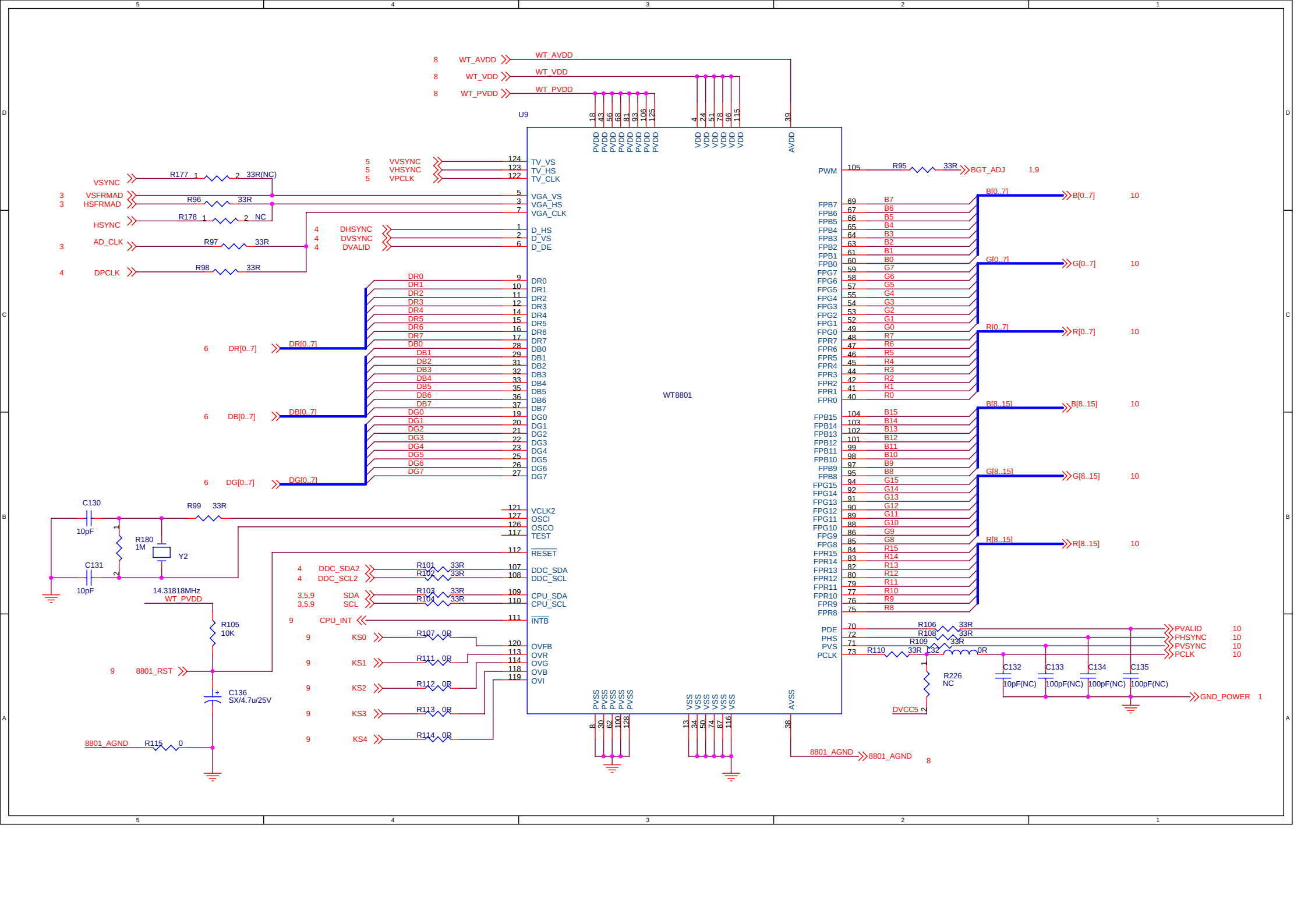


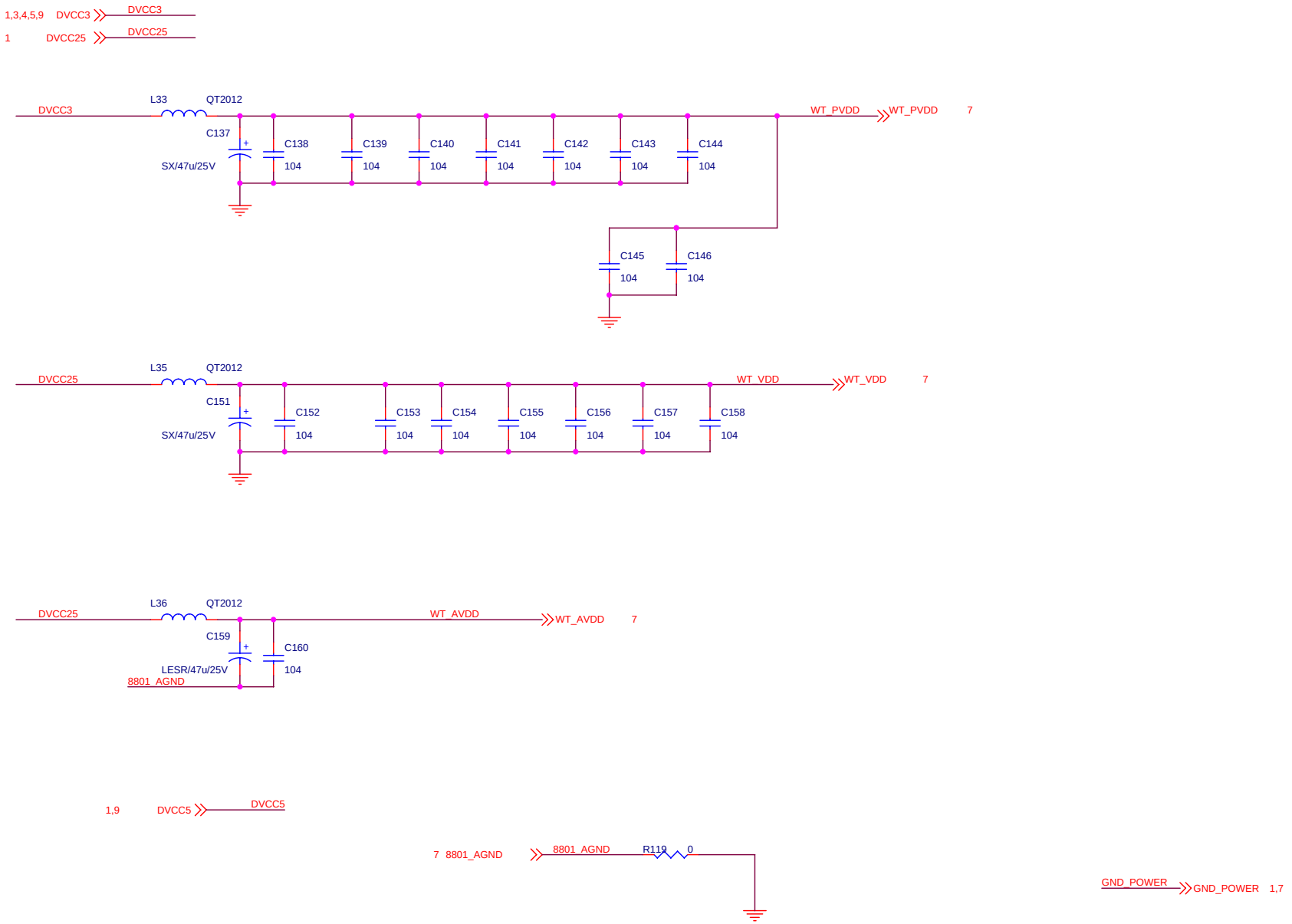
Digital Interface Damping Network



VIDEO Interface Damping Network




U9
WT8801
WT_AVDD
WT_VDD
WT_PVDD
VVSYNC
VHSYNC
VPCLK
TV_VS
TV_HS
TV_CLK
VGA_VS
VGA_HS
VGA_CLK
D_HS
D_VS
D_DE
DHSYNC
DVSYNC
DVALID
VSYNC
VSFRMAD
HSFRMAD
HSYNC
AD_CLK
DPCLK
R177 33R(NC)
R96 33R
R178 NC
R97 33R
R98 33R
DR[0..7]
DB[0..7]
DG[0..7]
C130 10pF
C131 10pF
R180 1M
Y2
14.31818MHz
R99 33R
VCLK2
OSCI
OSCO
TEST
RESET
DDC_SDA2
DDC_SCL2
DDC_SDA
DDC_SCL
SDA
SCL
CPU_SDA
CPU_SCL
CPU_INT
INTB
KS0
KS1
KS2
KS3
KS4
R101 33R
R102 33R
R103 33R
R104 33R
R107 0R
R111 0R
R112 0R
R113 0R
R114 0R
OVFB
OVR
OVG
OVB
OVI
R105 10K
8801_RST
C136 SX/4.7u/25V
8801_AGND
R115 0
PWM
R95 33R
BGT_ADJ
B[0..7]
G[0..7]
R[0..7]
B[8..15]
G[8..15]
R[8..15]
PDE
PHS
PVS
PCLK
R106 33R
R108 33R
R109 33R
R110 33R
R32 0R
R226 NC
DVCC5
C132 10pF(NC)
C133 100pF(NC)
C134 100pF(NC)
C135 100pF(NC)
PVALID
PHSYNC
PVSYNC
PCLK
GND_POWER
PVDD
VDD
AVDD
PVSS
VSS
AVSS


1,3,4,5,9 DVCC3 DVCC3
1 DVCC25 DVCC25
DVCC3 L33 QT2012 C137 SX/47u/25V C138 104 C139 104 C140 104 C141 104 C142 104 C143 104 C144 104 WT_PVDD WT_PVDD 7
C145 104 C146 104
DVCC25 L35 QT2012 C151 SX/47u/25V C152 104 C153 104 C154 104 C155 104 C156 104 C157 104 C158 104 WT_VDD WT_VDD 7
DVCC25 L36 QT2012 C159 LESR/47u/25V 8801_AGND C160 104 WT_AVDD WT_AVDD 7
1,9 DVCC5 DVCC5
7 8801_AGND 8801_AGND R119 0
GND_POWER GND_POWER 1,7

1 PVCC → PVCC

1,3,4,5,8,9 DVCC3 → DVCC3

1,7 GND_POWER → GND_POWER

7 PCLK → PCLK

7 PHSYNC → PHSYNC

7 PVSYNC → PVSYNC

7 PVALID → PVALID

9 EN_LVDS → EN_LVDS

7 R[0..7] → R[0..7]

RA15: R0 R1 R2 R3 → RP0 RP1 RP2 RP3 — 33R/8P4R — CA1 C*4_10pF(NC)

RA16: R4 R5 R6 R7 → RP4 RP5 RP6 RP7 — 33R/8P4R — CA2 C*4_10pF(NC)

7 G[0..7] → G[0..7]

RA17: G0 G1 G2 G3 → GP0 GP1 GP2 GP3 — 33R/8P4R — CA3 C*4_10pF(NC)

RA18: G4 G5 G6 G7 → GP4 GP5 GP6 GP7 — 33R/8P4R — CA4 C*4_10pF(NC)

7 B[0..7] → B[0..7]

RA19: B0 B1 B2 B3 → BP0 BP1 BP2 BP3 — 33R/8P4R — CA5 C*4_10pF(NC)

RA20: B4 B5 B6 B7 → BP4 BP5 BP6 BP7 — 33R/8P4R — CA6 C*4_10pF(NC)

7 R[8..15] → R[8..15]

RA21: R8 R9 R10 R11 → RP8 RP9 RP10 RP11 — 33R/8P4R — CA7 C*4_10pF(NC)

RA22: R12 R13 R14 R15 → RP12 RP13 RP14 RP15 — 33R/8P4R — CA8 C*4_10pF(NC)

7 G[8..15] → G[8..15]

RA23: G8 G9 G10 G11 → GP8 GP9 GP10 GP11 — 33R/8P4R — CA9 C*4_10pF(NC)

RA24: G12 G13 G14 G15 → GP12 GP13 GP14 GP15 — 33R/8P4R — CA10 C*4_10pF(NC)

7 B[8..15] → B[8..15]

RA25: B8 B9 B10 B11 → BP8 BP9 BP10 BP11 — 33R/8P4R — CA11 C*4_10pF(NC)

RA26: B12 B13 B14 B15 → BP12 BP13 BP14 BP15 — 33R/8P4R — CA12 C*4_10pF(NC)

CPT 15" PANEL USED

P3

BP15 BP14 BP13 BP12 BP11 BP10 BP9 BP8 GP15 GP14 GP13 GP12 GP11 GP10 GP9 GP8 RP15 RP14 RP13 RP12 RP11 RP10 RP9 RP8

IL-FHR30S-HF(JAE)/CPT(NC)

P2

PCLK PVALID PVSYNC PHSYNC BP7 BP6 BP5 BP4 BP3 BP2 BP1 BP0 GP7 GP6 GP5 GP4 GP3 GP2 GP1 GP0 RP7 RP6 RP5 RP4 RP3 RP2 RP1 RP0 PVCC

IL-FHR45S-HF(JAE)/CPT(NC)

GENERAL PANEL CONNECTOR USED

P4

BP7 BP5 BP3 BP1 GP7 GP5 GP3 GP1 RP7 RP5 RP3 RP1 PVALID PVSYNC PVCC

BP6 BP4 BP2 BP0 GP6 GP4 GP2 GP0 RP6 RP4 RP2 RP0 EN_LVDS PHSYNC PCLK PVCC

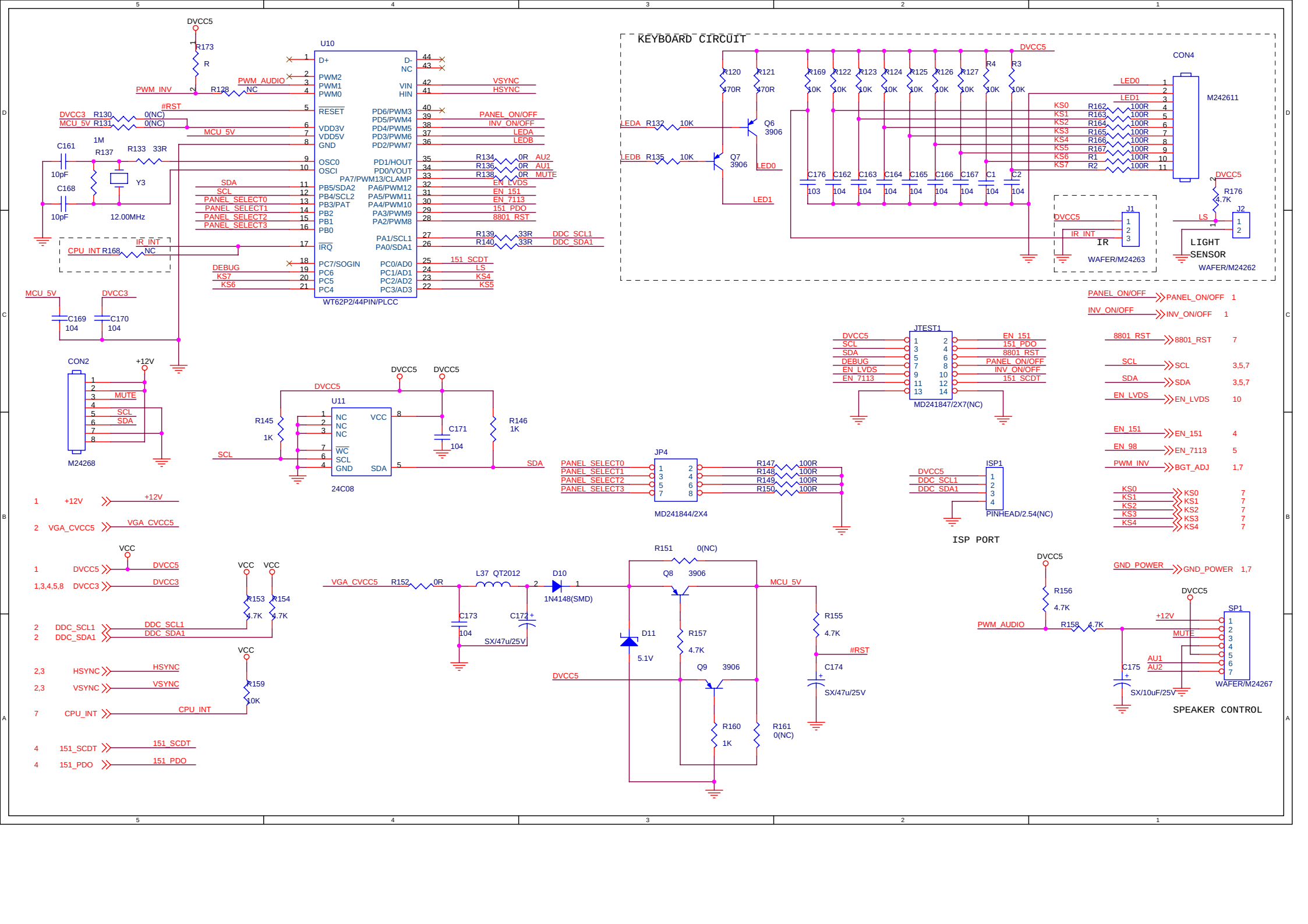
MD240220

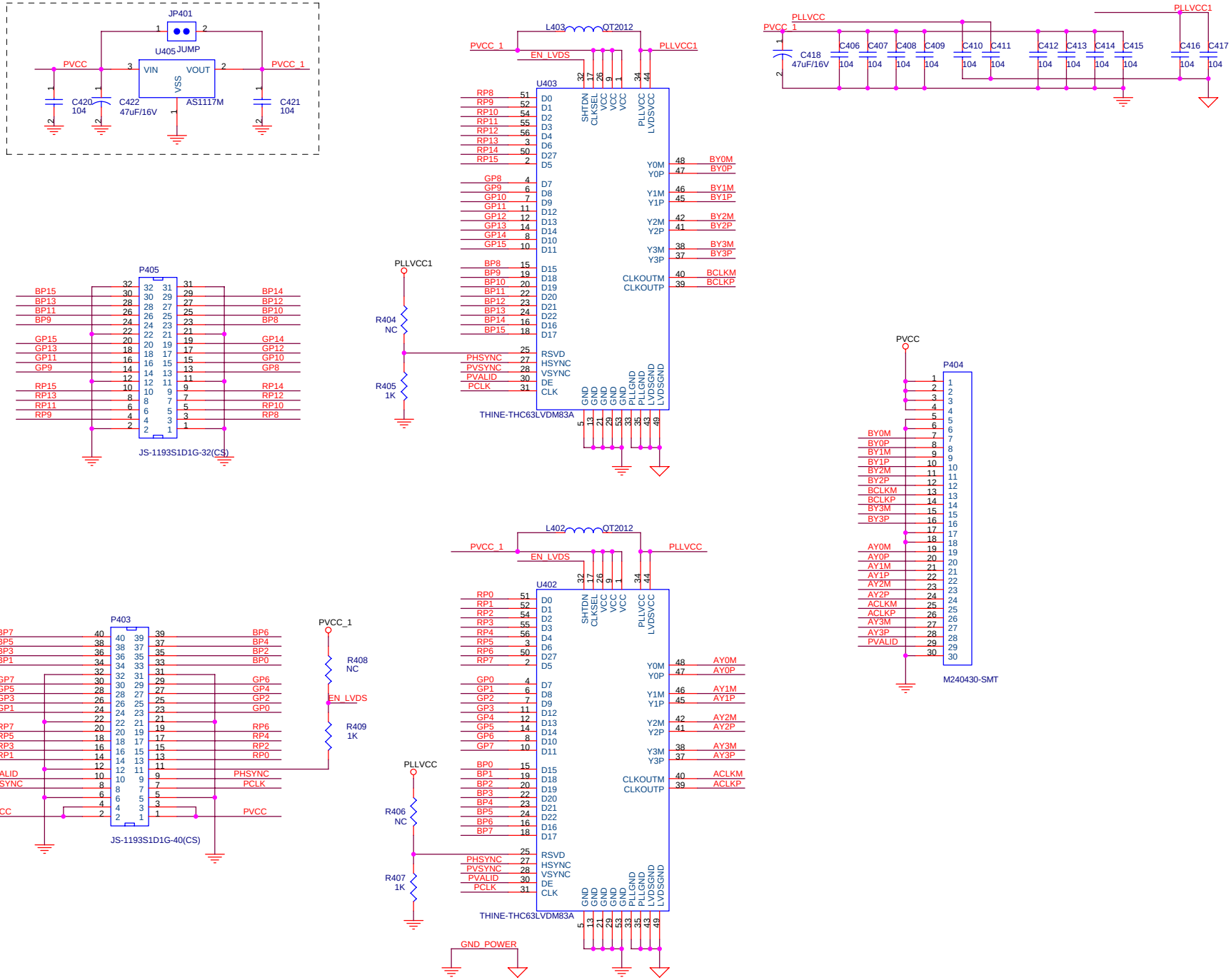
P5

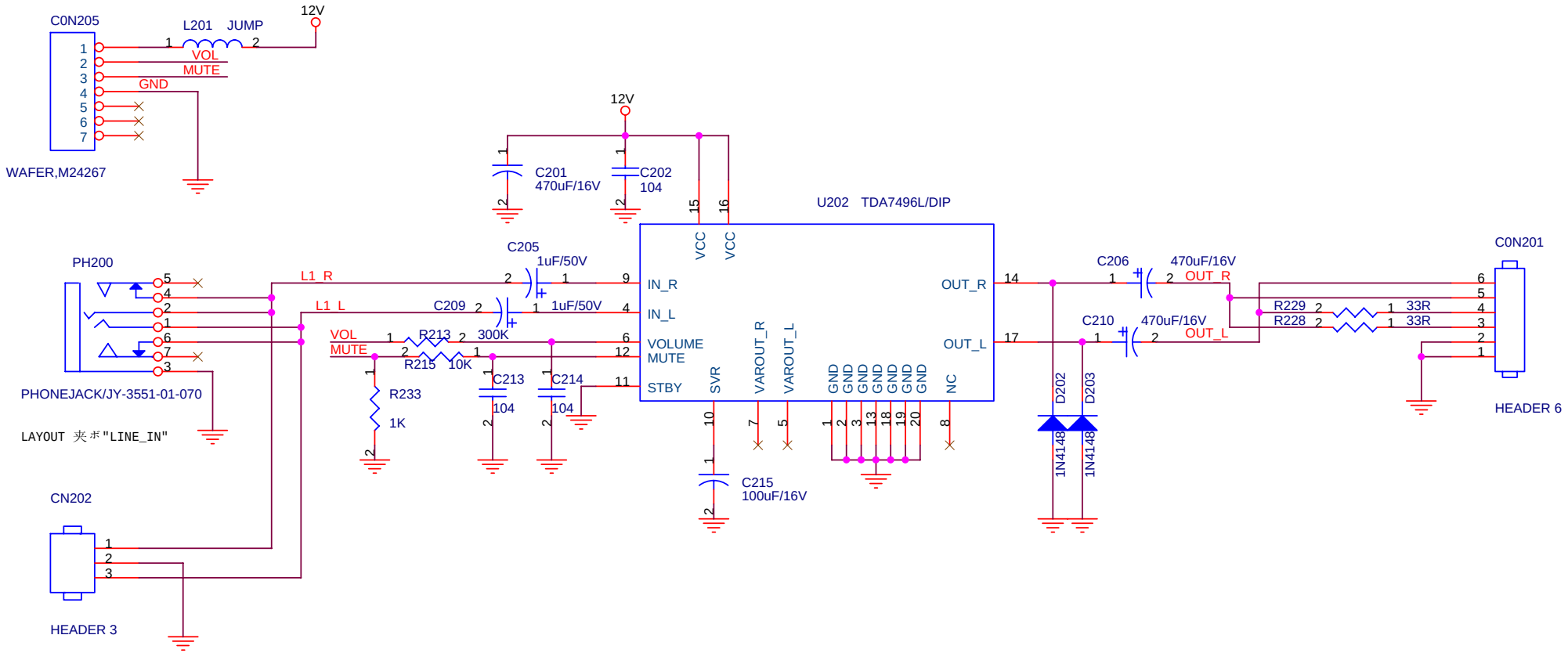
BP15 BP13 BP11 BP9 GP15 GP13 GP11 GP9 RP15 RP13 RP11 RP9

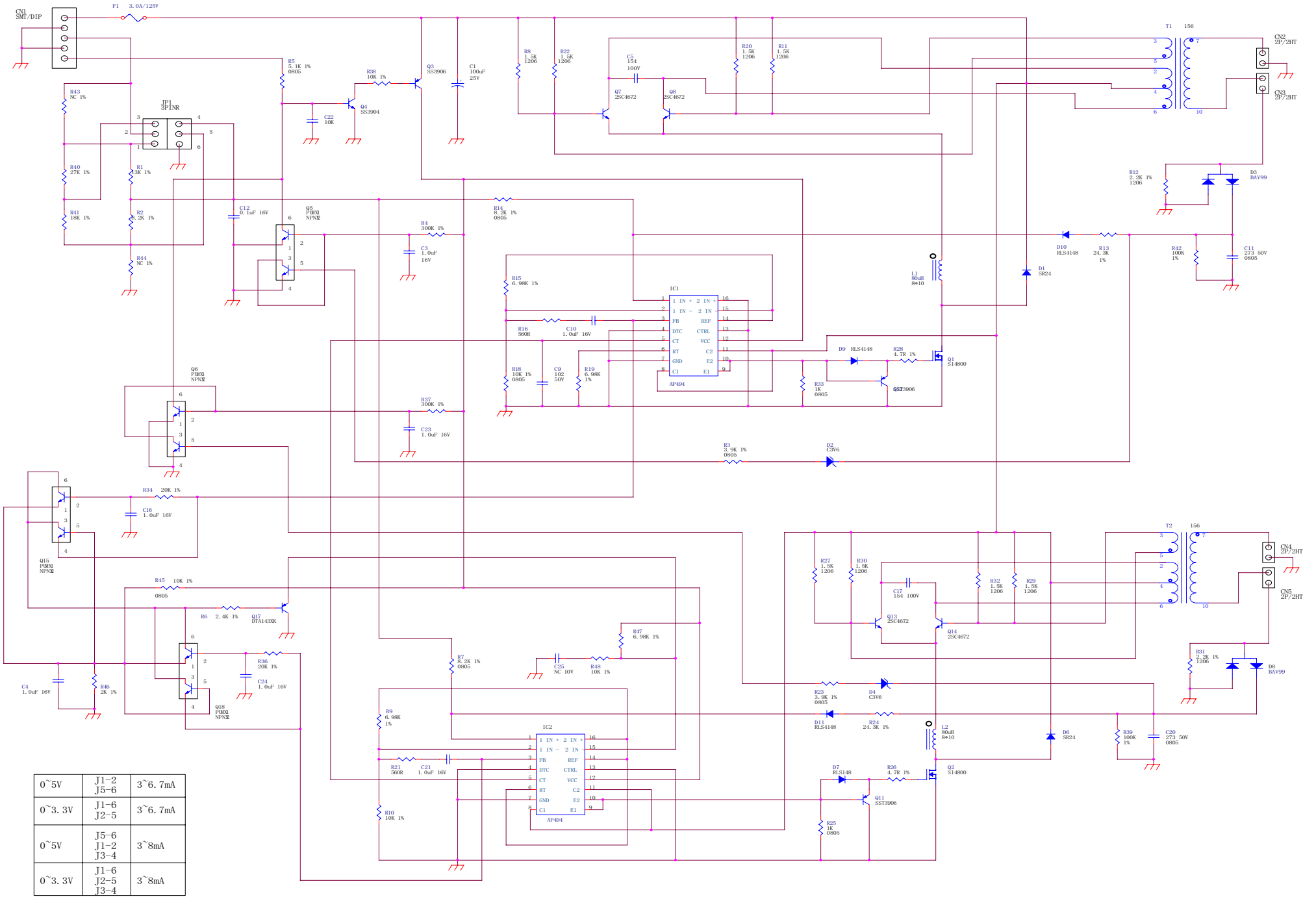
BP14 BP12 BP10 BP8 GP14 GP12 GP10 GP8 RP14 RP12 RP10 RP8

MD240216

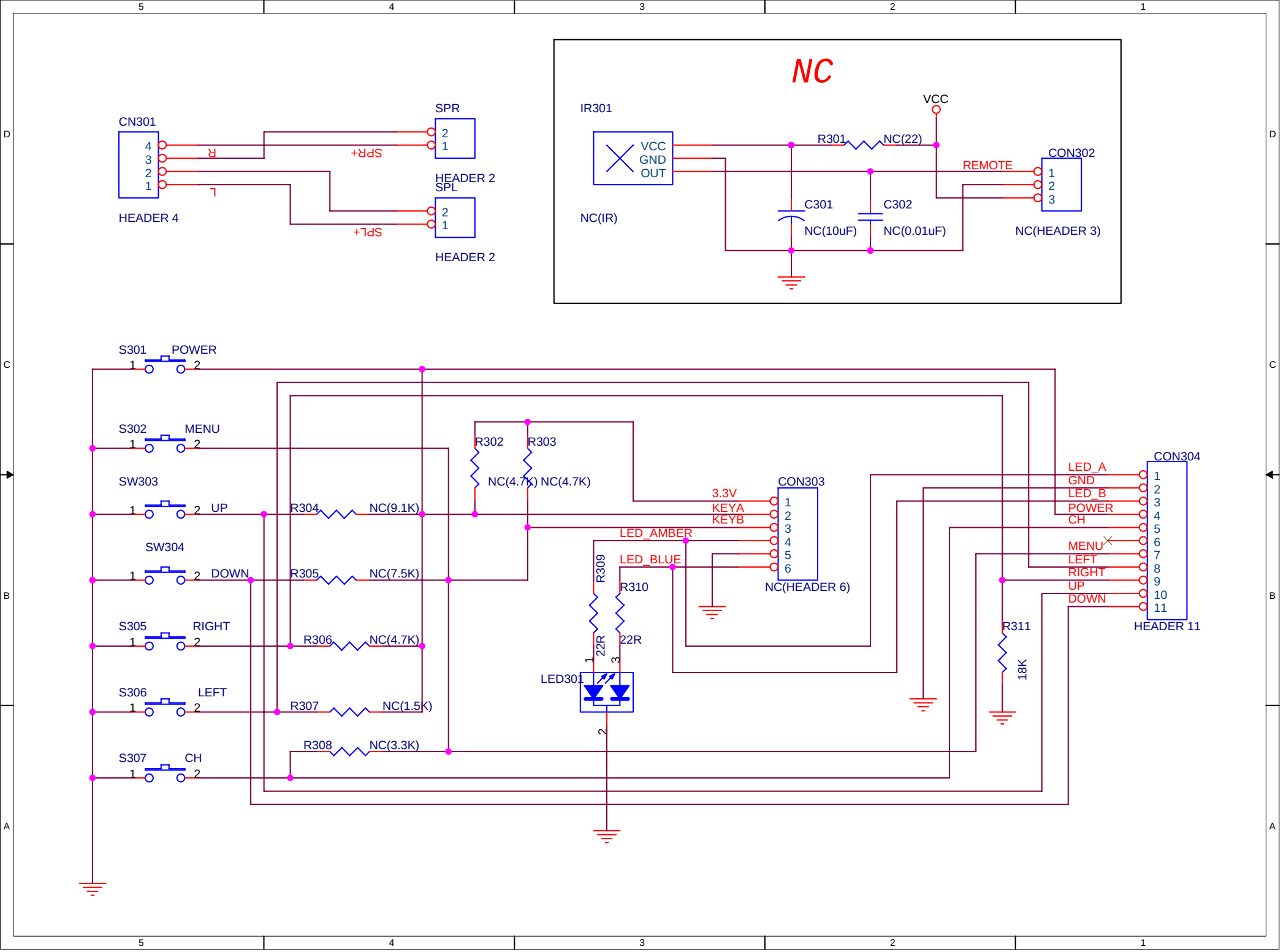

KEYBOARD CIRCUIT
U10
WT62P2/44PIN/PLCC
U11
24C08
CON4
M242611
CON2
M24268
JTEST1
MD241847/2X7(NC)
JP4
MD241844/2X4
ISP1
PINHEAD/2.54(NC)
ISP PORT
J1
IR
WAFER/M24263
J2
LIGHT SENSOR
WAFER/M24262
SP1
WAFER/M24267
SPEAKER CONTROL
L37 QT2012
D10
1N4148(SMD)
D11
5.1V
Q6 3906
Q7 3906
Q8 3906
Q9 3906
Y3
12.00MHz
DVCC5
DVCC3
MCU_5V
+12V
VCC
VGA_CVCC5
PANEL_ON/OFF
INV_ON/OFF
8801_RST
SCL
SDA
EN_LVDS
EN_151
EN_98
PWM_INV
BGT_ADJ
KS0
KS1
KS2
KS3
KS4
GND_POWER
PWM_AUDIO
HSYNC
VSYNC
CPU_INT
151_SCDT
151_PDO
DDC_SCL1
DDC_SDA1
LEDA
LEDB
LED0
LED1
#RST
MUTE
AU1
AU2
LS
IR_INT
DEBUG
EN_7113
EN_151
151_PDO
8801_RST
PANEL_SELECT0
PANEL_SELECT1
PANEL_SELECT2
PANEL_SELECT3

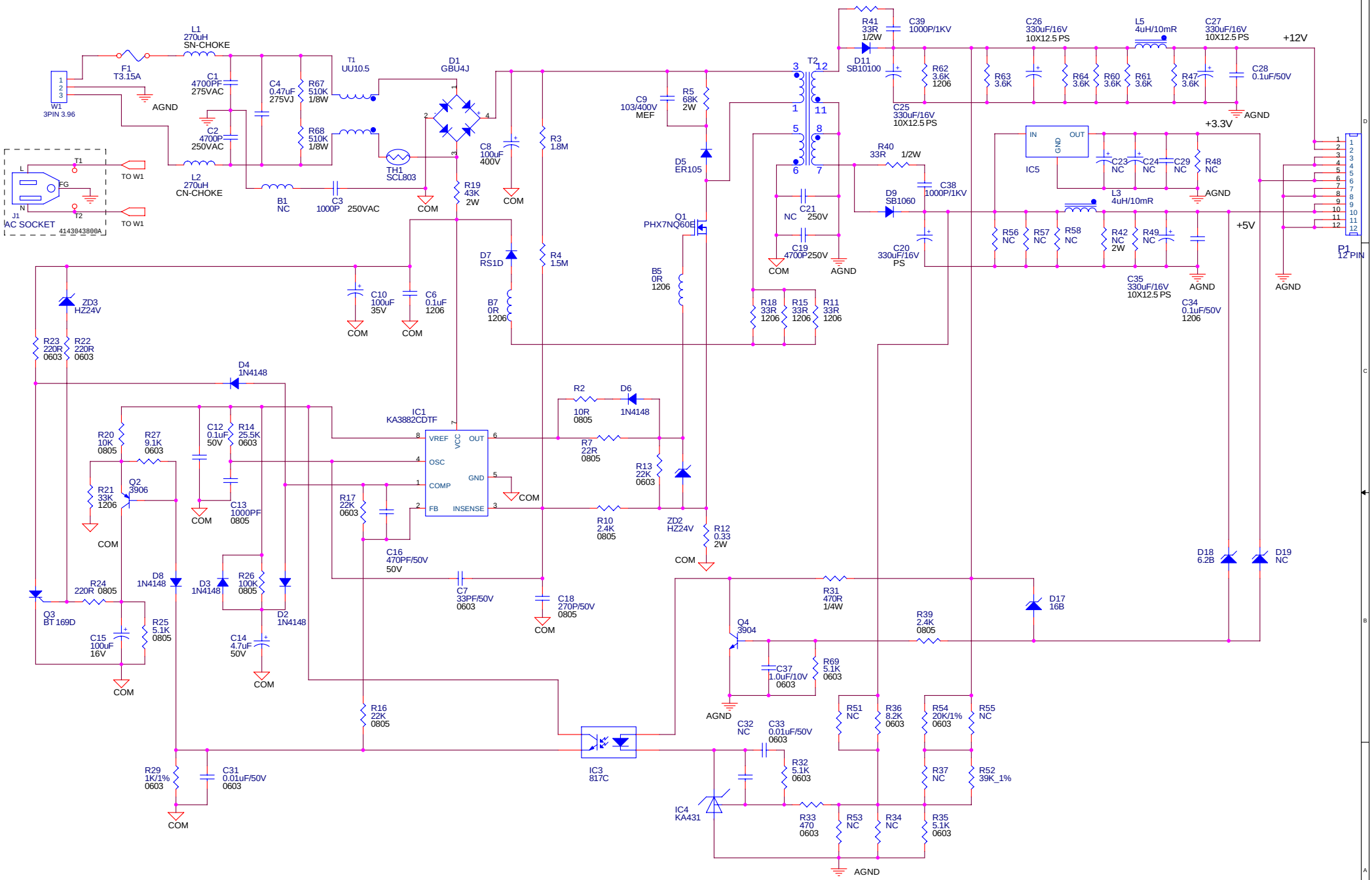

JP401
JUMP
U405
AS1117M
PVCC
PVCC_1
VIN
VOUT
VSS
C420 104
C422 47uF/16V
C421 104
L403 QT2012
U403
THINE-THC63LVDM83A
PLLVCC1
EN_LVDS
R404 NC
R405 1K
P405
JS-1193S1D1G-32(CS)
L402 QT2012
U402
PLLVCC
R406 NC
R407 1K
R408 NC
R409 1K
P403
JS-1193S1D1G-40(CS)
GND_POWER
C418 47uF/16V
C406 104
C407 104
C408 104
C409 104
C410 104
C411 104
C412 104
C413 104
C414 104
C415 104
C416 104
C417 104
P404
M240430-SMT


C0N205
L201 JUMP
12V
VOL
MUTE
GND
WAFER,M24267
C201
470uF/16V
C202
104
U202 TDA7496L/DIP
VCC
VCC
IN_R
IN_L
VOLUME
MUTE
STBY
SVR
VAROUT_R
VAROUT_L
GND
NC
OUT_R
OUT_L
PH200
PHONEJACK/JY-3551-01-070
LAYOUT 夹ボ"LINE_IN"
L1 R
L1 L
C205
1uF/50V
C209
1uF/50V
R213 300K
R215 10K
R233
1K
C213
104
C214
104
C215
100uF/16V
C206
470uF/16V
OUT R
C210
470uF/16V
OUT_L
D202
D203
1N4148
1N4148
R229 33R
R228 33R
C0N201
HEADER 6
CN202
HEADER 3



0~5V	J1-2 J5-6	3~6.7mA
0~3.3V	J1-6 J2-5	3~6.7mA
0~5V	J5-6 J1-2 J3-4	3~8mA
0~3.3V	J1-6 J2-5 J3-4	3~8mA


NC
CN301
HEADER 4
SPR
SPR+
HEADER 2
SPL
SPL+
HEADER 2
IR301
VCC
GND
OUT
NC(IR)
R301
NC(22)
C301
NC(10uF)
C302
NC(0.01uF)
REMOTE
CON302
NC(HEADER 3)
S301 POWER
S302 MENU
SW303
UP
SW304
DOWN
S305 RIGHT
S306 LEFT
S307 CH
R304 NC(9.1K)
R305 NC(7.5K)
R306 NC(4.7K)
R307 NC(1.5K)
R308 NC(3.3K)
R302 NC(4.7K)
R303 NC(4.7K)
3.3V
KEYA
KEYB
CON303
NC(HEADER 6)
LED_AMBER
LED_BLUE
R309 22R
R310 22R
LED301
R311 18K
CON304
LED_A
GND
LED_B
POWER
CH
MENU
LEFT
RIGHT
UP
DOWN
HEADER 11


L1 270uH SN-CHOKE
F1 T3.15A
W1 3PIN 3.96
AGND
C1 4700PF 275VAC
C2 4700P 250VAC
L2 270uH CN-CHOKE
C4 0.47uF 275VJ
R67 510K 1/8W
R68 510K 1/8W
T1 UU10.5
B1 NC
C3 1000P 250VAC
TH1 SCL803
D1 GBU4J
C8 100uF 400V
R19 43K 2W
R3 1.8M
R4 1.5M
COM
J1 AC SOCKET
4143043800A
TO W1
C9 103/400V MEF
R5 68K 2W
D5 ER105
Q1 PHX7NQ60E
B5 0R 1206
B7 0R 1206
D7 RS1D
C10 100uF 35V
C6 0.1uF 1206
ZD3 HZ24V
R23 220R 0603
R22 220R 0603
D4 1N4148
IC1 KA3882CDTF
VREF
VCC
OUT
OSC
COMP
GND
FB
INSENSE
R20 10K 0805
R27 9.1K 0603
R21 33K 1206
Q2 3906
C12 0.1uF 50V
R14 25.5K 0603
C13 1000PF 0805
R17 22K 0603
C16 470PF/50V 50V
R2 10R 0805
D6 1N4148
R7 22R 0805
R13 22K 0603
R10 2.4K 0805
ZD2 HZ24V
R12 0.33 2W
C7 33PF/50V 0603
C18 270P/50V 0805
R24 220R 0805
Q3 BT 169D
C15 100uF 16V
R25 5.1K 0805
D8 1N4148
D3 1N4148
R26 100K 0805
D2 1N4148
C14 4.7uF 50V
R16 22K 0805
R29 1K/1% 0603
C31 0.01uF/50V 0603
T2
C21 NC 250V
C19 4700P250V
R41 33R 1/2W
C39 1000P/1KV
D11 SB10100
C25 330uF/16V 10X12.5 PS
R62 3.6K 1206
R40 33R 1/2W
D9 SB1060
C38 1000P/1KV
C20 330uF/16V PS
R18 33R 1206
R15 33R 1206
R11 33R 1206
C26 330uF/16V 10X12.5 PS
R63 3.6K
R64 3.6K
R60 3.6K
R61 3.6K
R47 3.6K
L5 4uH/10mR
C27 330uF/16V 10X12.5 PS
C28 0.1uF/50V
+12V
+3.3V
+5V
IC5
IN
OUT
GND
C23 NC
C24 NC
C29 NC
R48 NC
L3 4uH/10mR
R56 NC
R57 NC
R58 NC
R42 NC 2W
R49 NC
C35 330uF/16V 10X12.5 PS
C34 0.1uF/50V 1206
P1 12 PIN
D18 6.2B
D19 NC
D17 16B
R31 470R 1/4W
R39 2.4K 0805
Q4 3904
C37 1.0uF/10V 0603
R69 5.1K 0603
C32 NC
C33 0.01uF/50V 0603
R32 5.1K 0603
R33 470 0603
R53 NC
R34 NC
R35 5.1K 0603
R51 NC
R36 8.2K 0603
R54 20K/1% 0603
R55 NC
R37 NC
R52 39K_1%
IC3 817C
IC4 KA431
Title
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Size
Document Number
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